

FIGURE 1

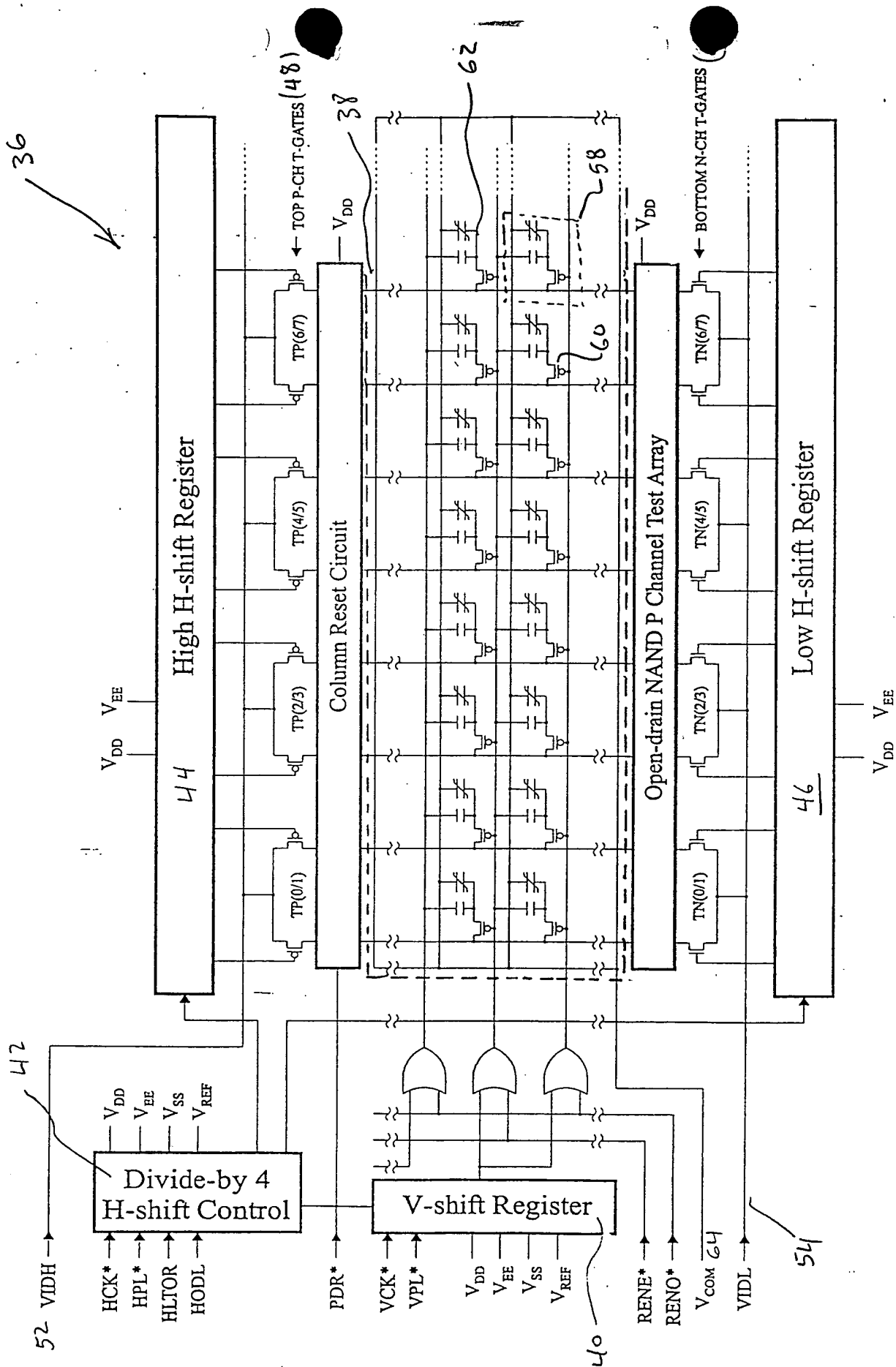


FIGURE 2A

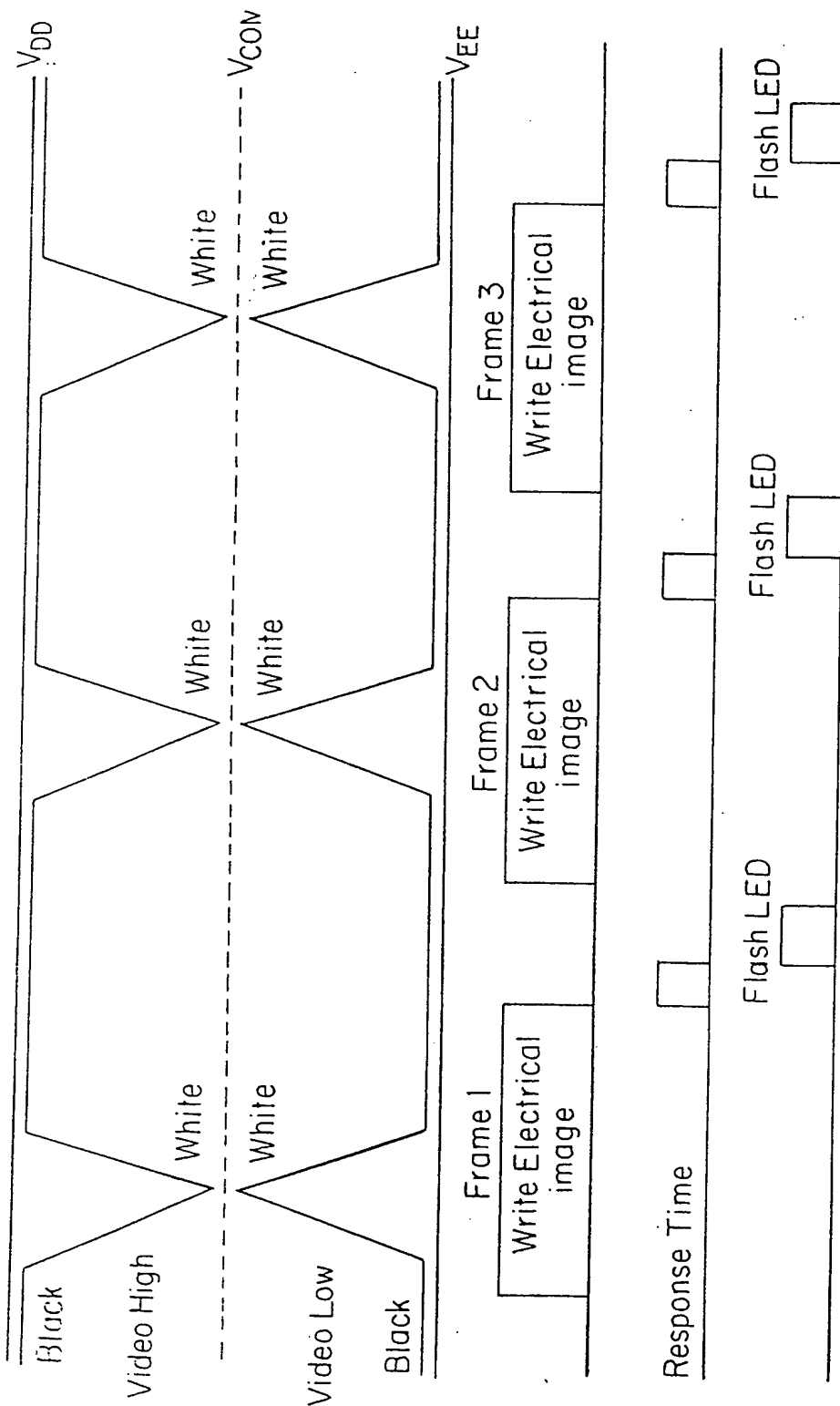


FIGURE 2B

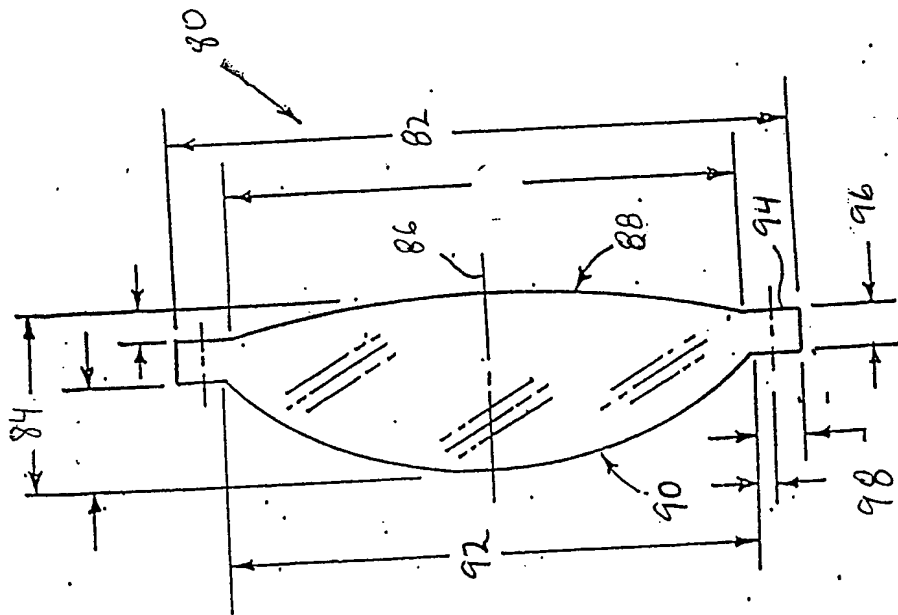


FIGURE 3A

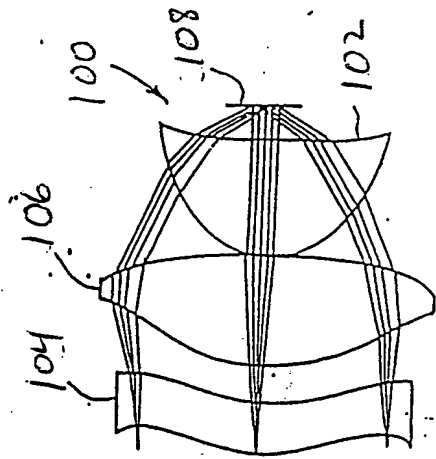


FIGURE 3B

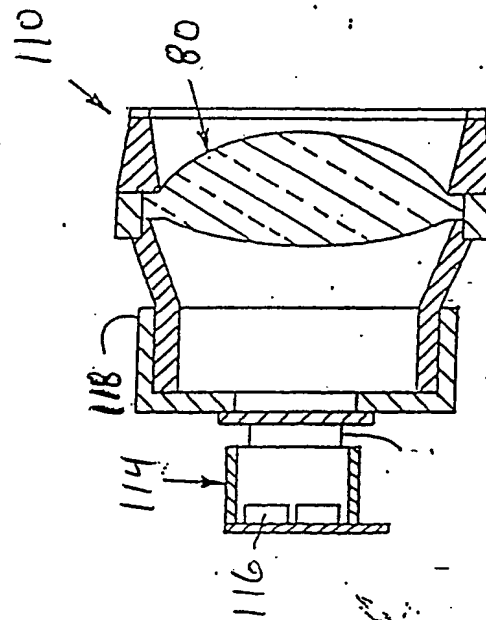


FIGURE 3C

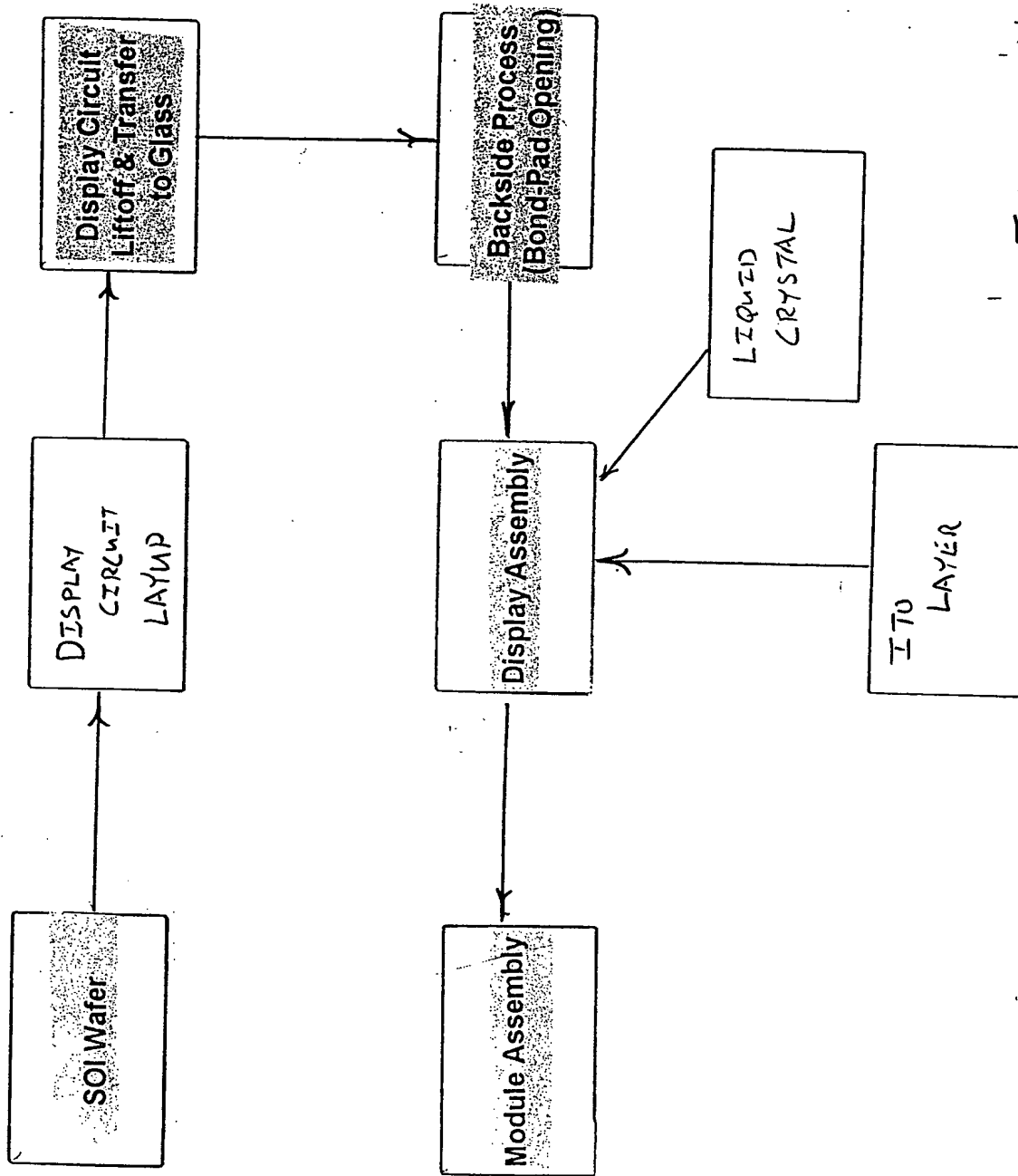
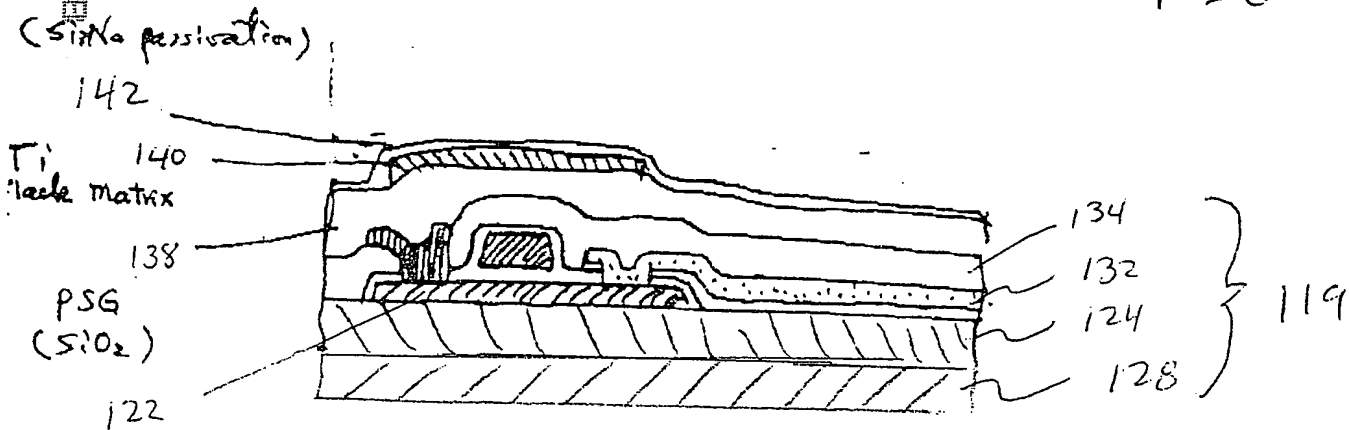
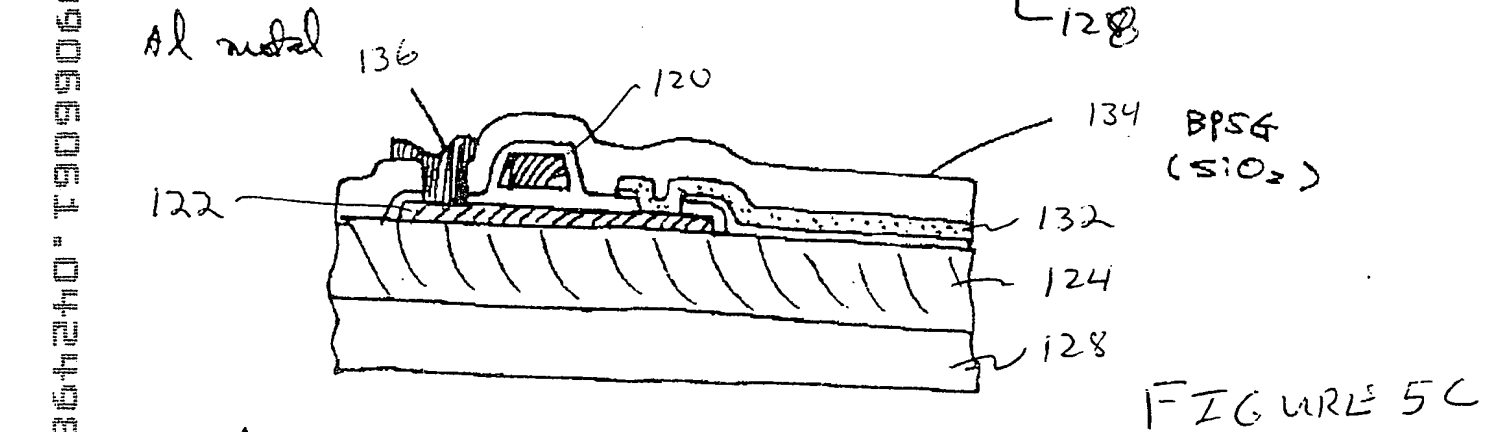
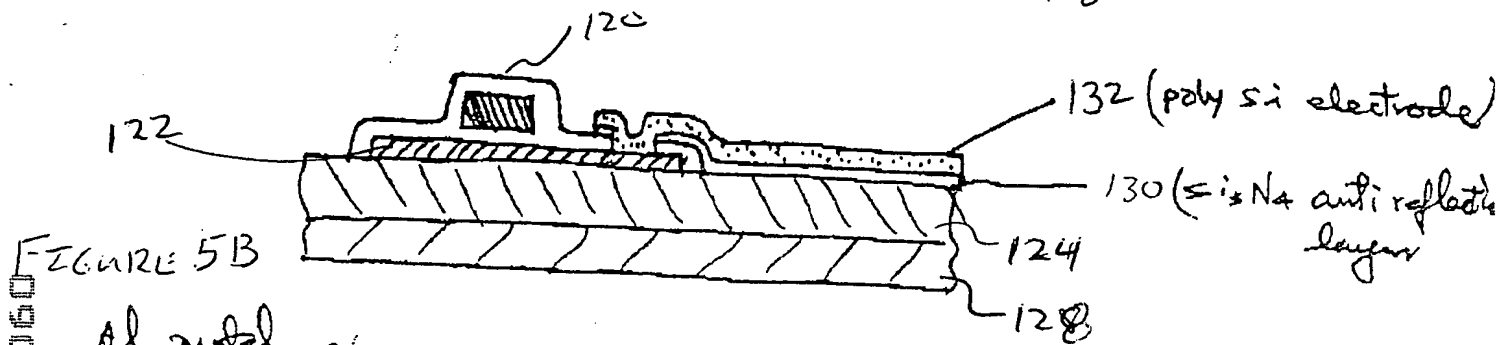
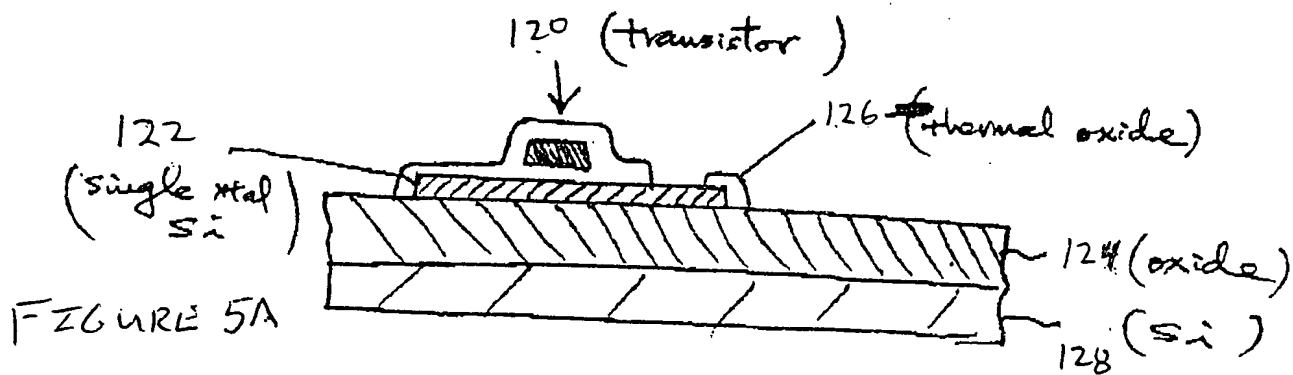
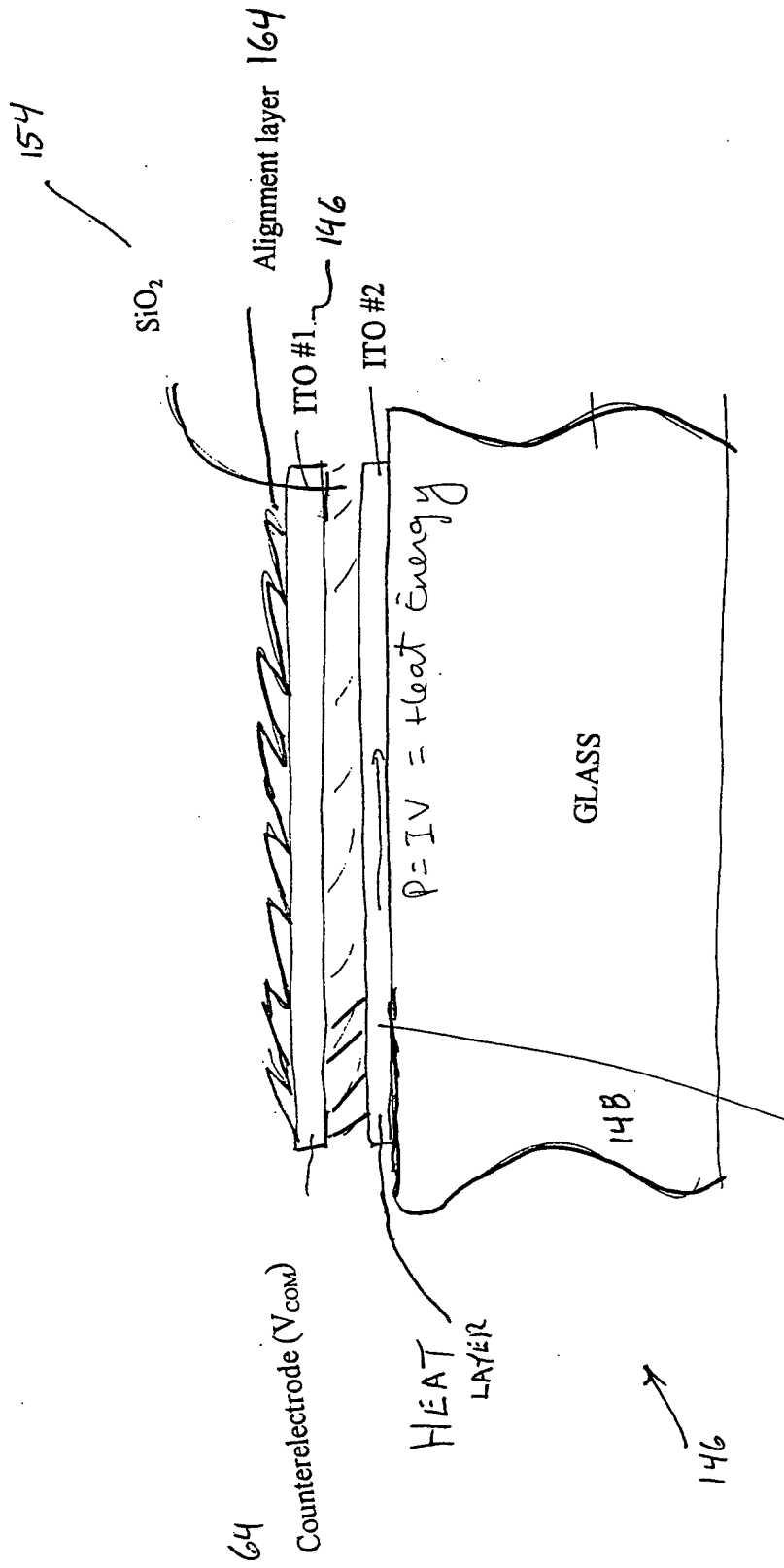


FIGURE 4





Patterned such that it covering only the pixel arrays when displayed formed.

FIGURE 6

090606J 040000Z

(sinopsis)

Ti-lack matrix

221

PSG (2015)

124

FIGURE 7

95 (page)

(copy)

6711 ~ 119

164

 $1.8 \mu m$

162

IT^o

IT^o

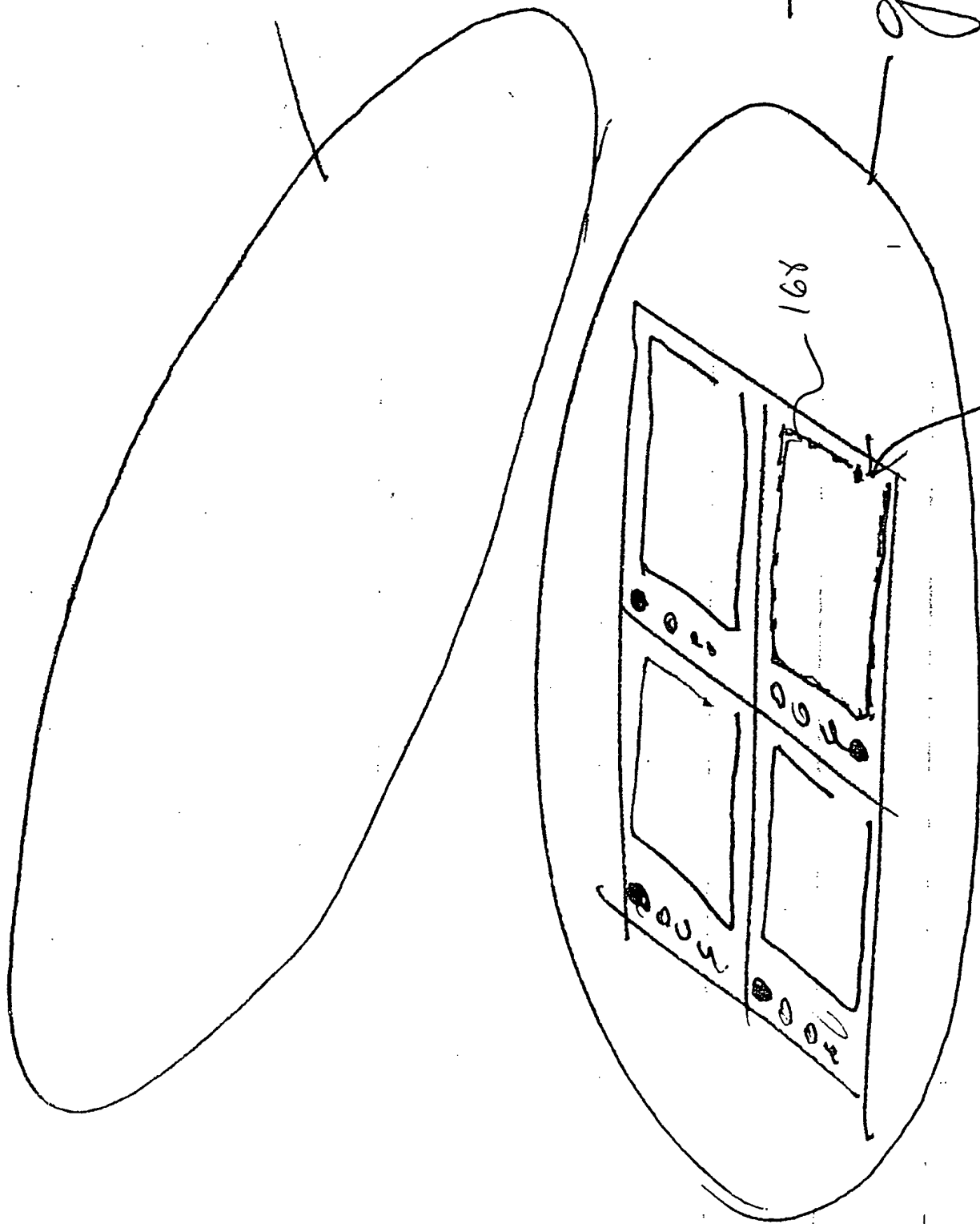
864240 T9099060

ITO
WAFER
146

TFT
glass

FIGURE 8

fill hole



SECRET

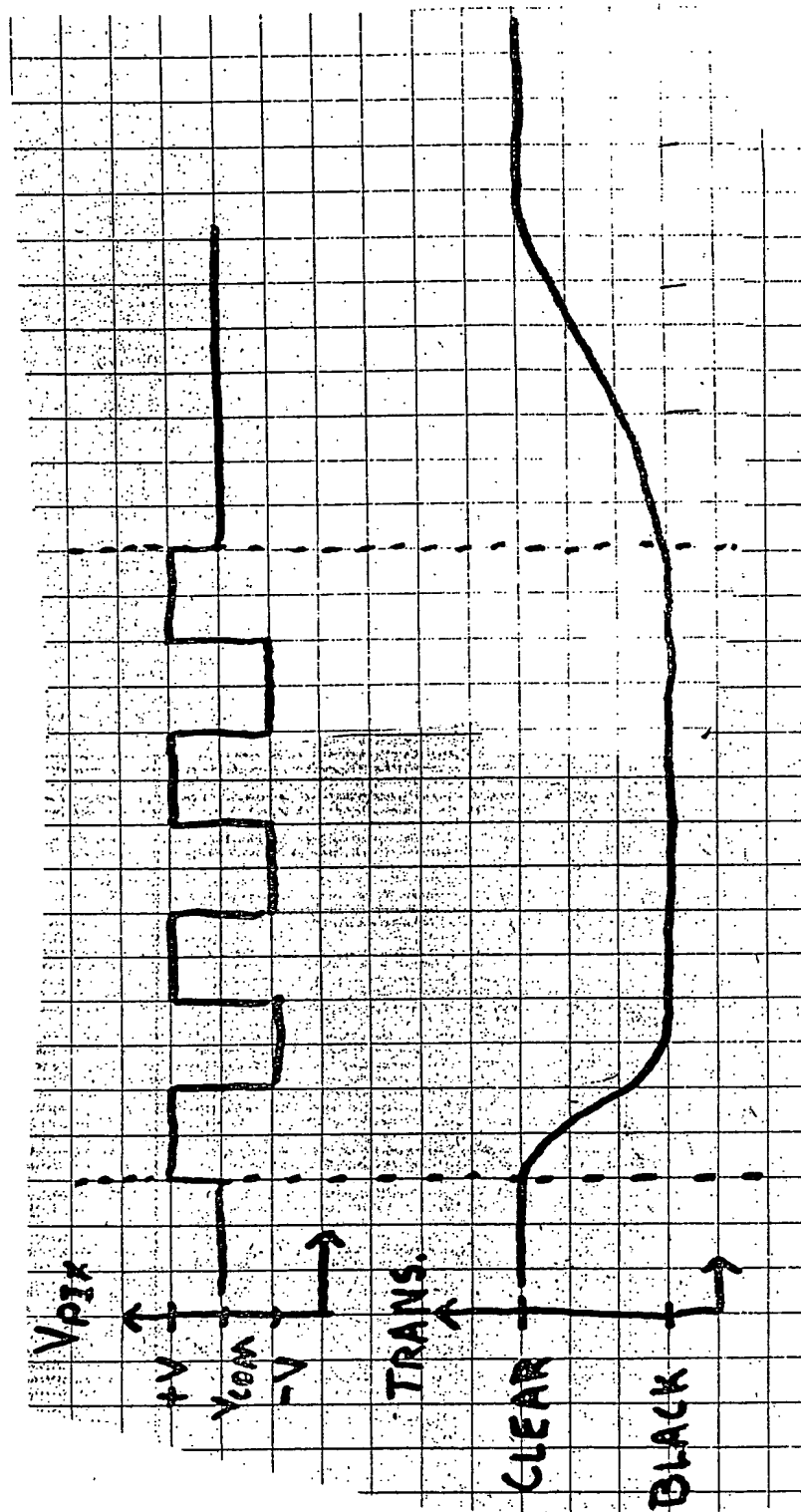
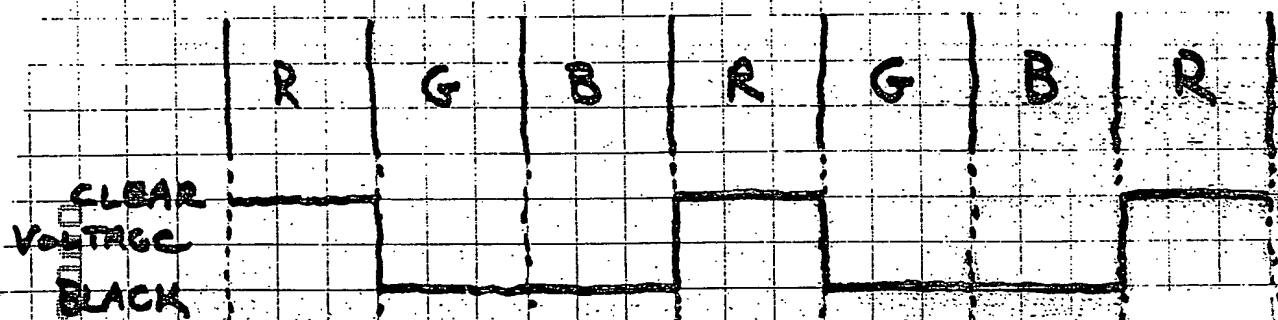


FIGURE 10

SINGLE RED PIXEL



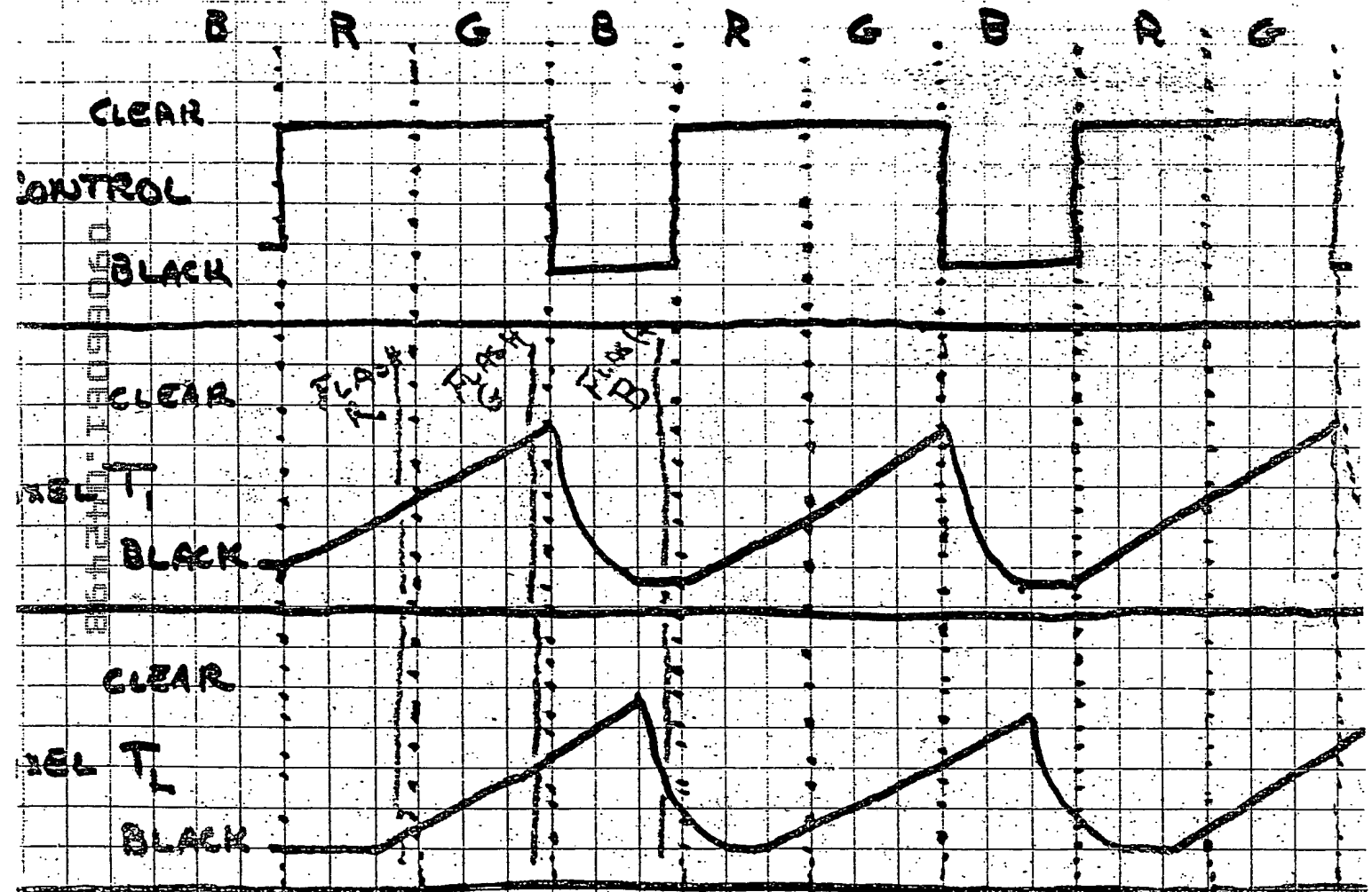
MAX CLEAR
TRANS
MAX BLACK

CURVE STARTED FROM MAX CLEAR

- NO STEADY STATE CONDITION

FIGURE 11A

INTERMEDIATE COLOR (RGB mix) EX: YELLOW



- Pixel T_L is DELAYED By 3ms BECAUSE OF WRITE TIME

FIGURE 11B

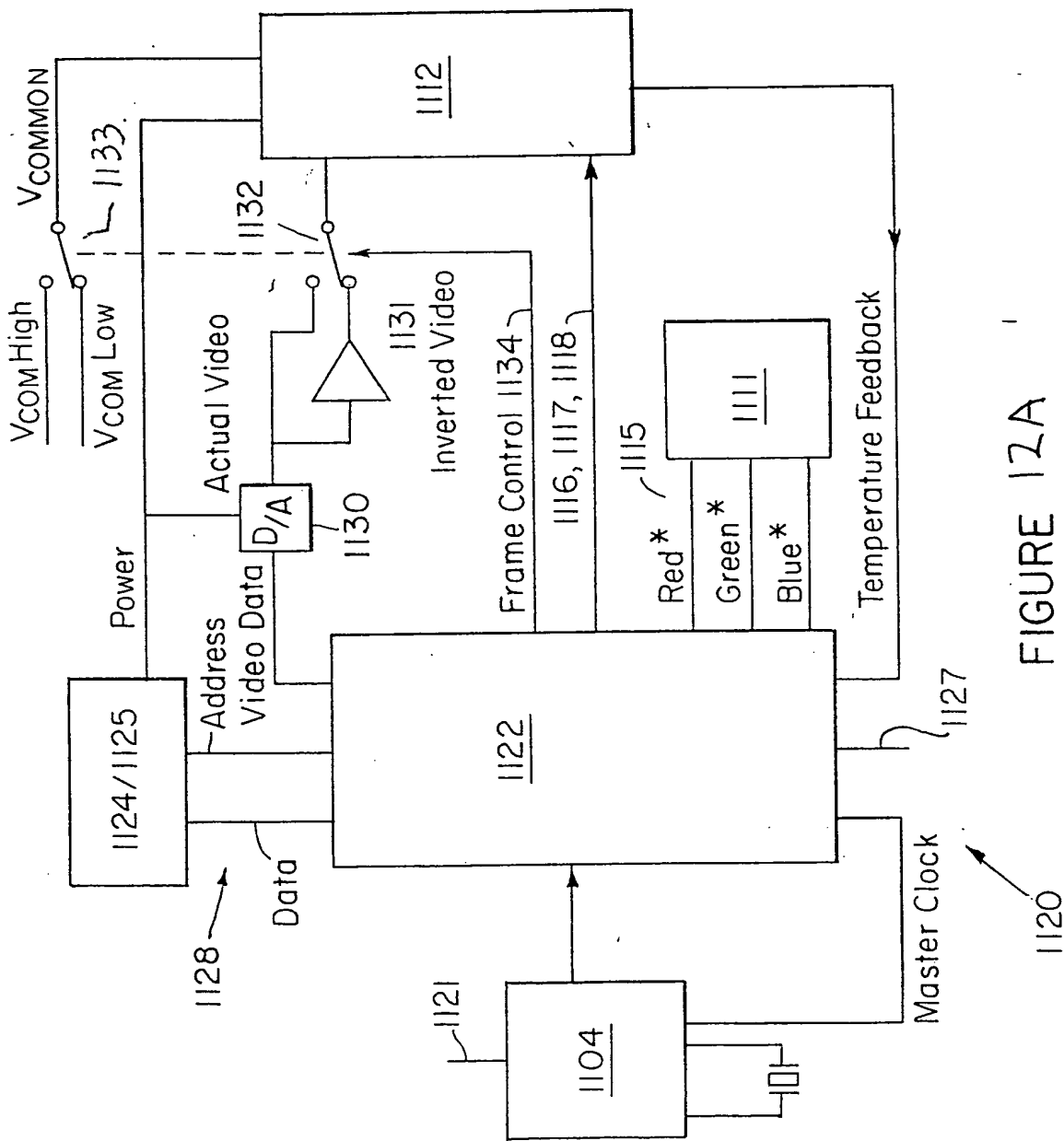


FIGURE 12A

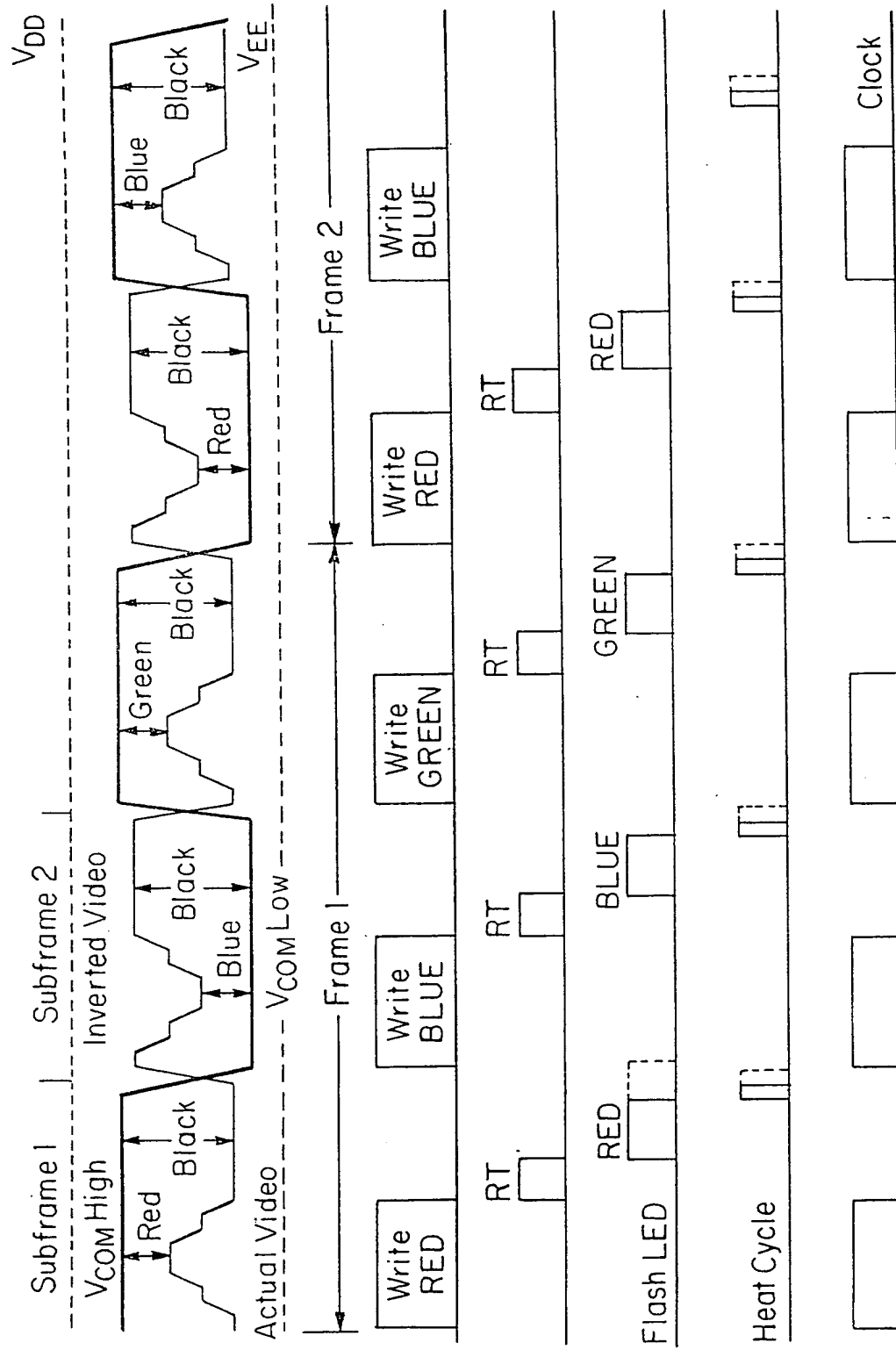


FIGURE 12B

V_{com} MODULATION

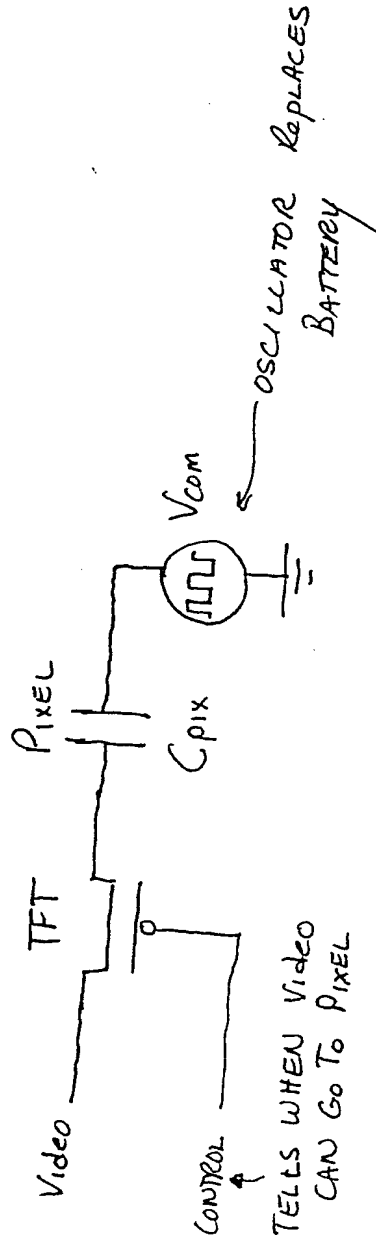


FIGURE 12C

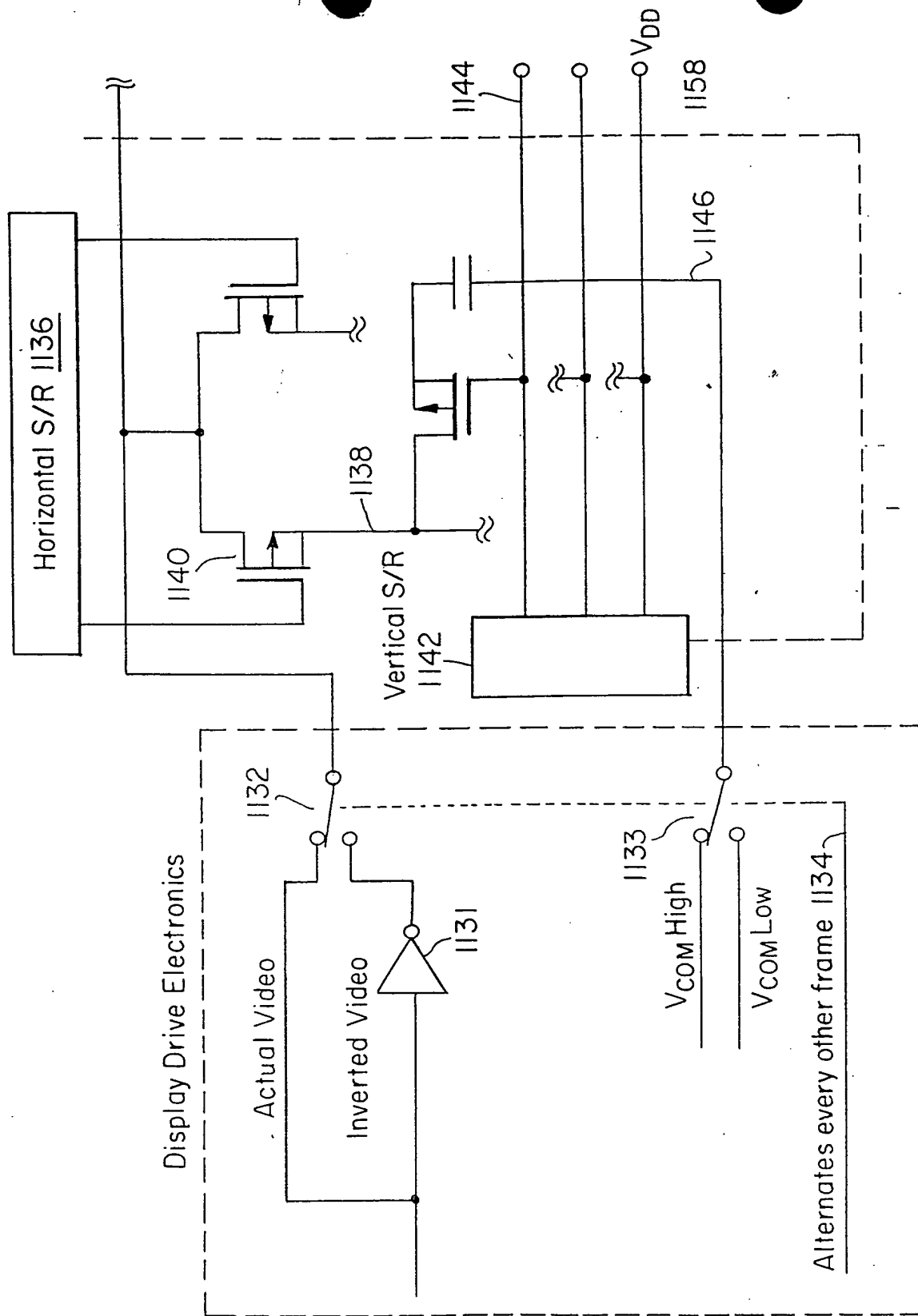


FIGURE 12D

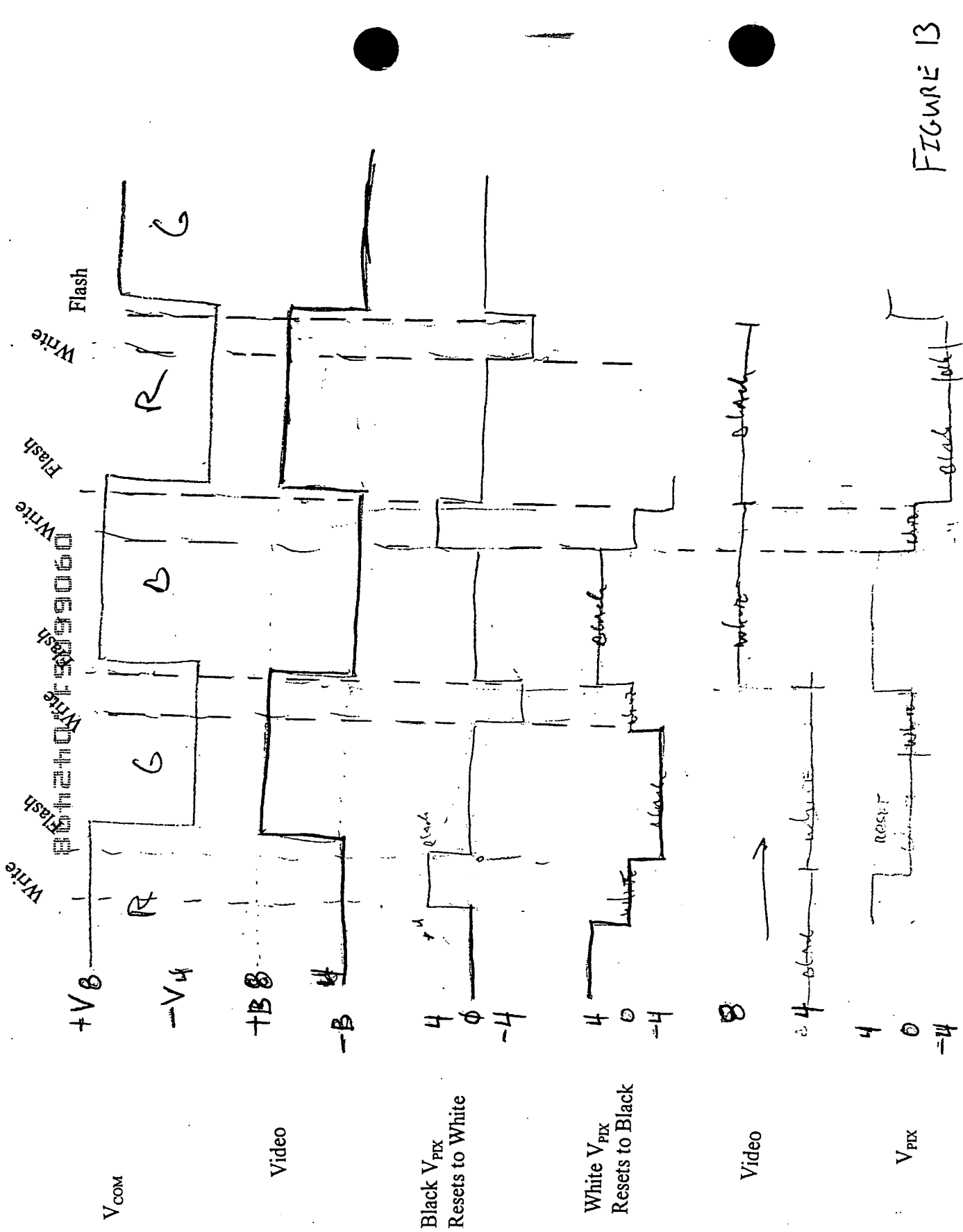


FIGURE 13

WRITE VIDEO → WAIT 100ns → RESET → RESET

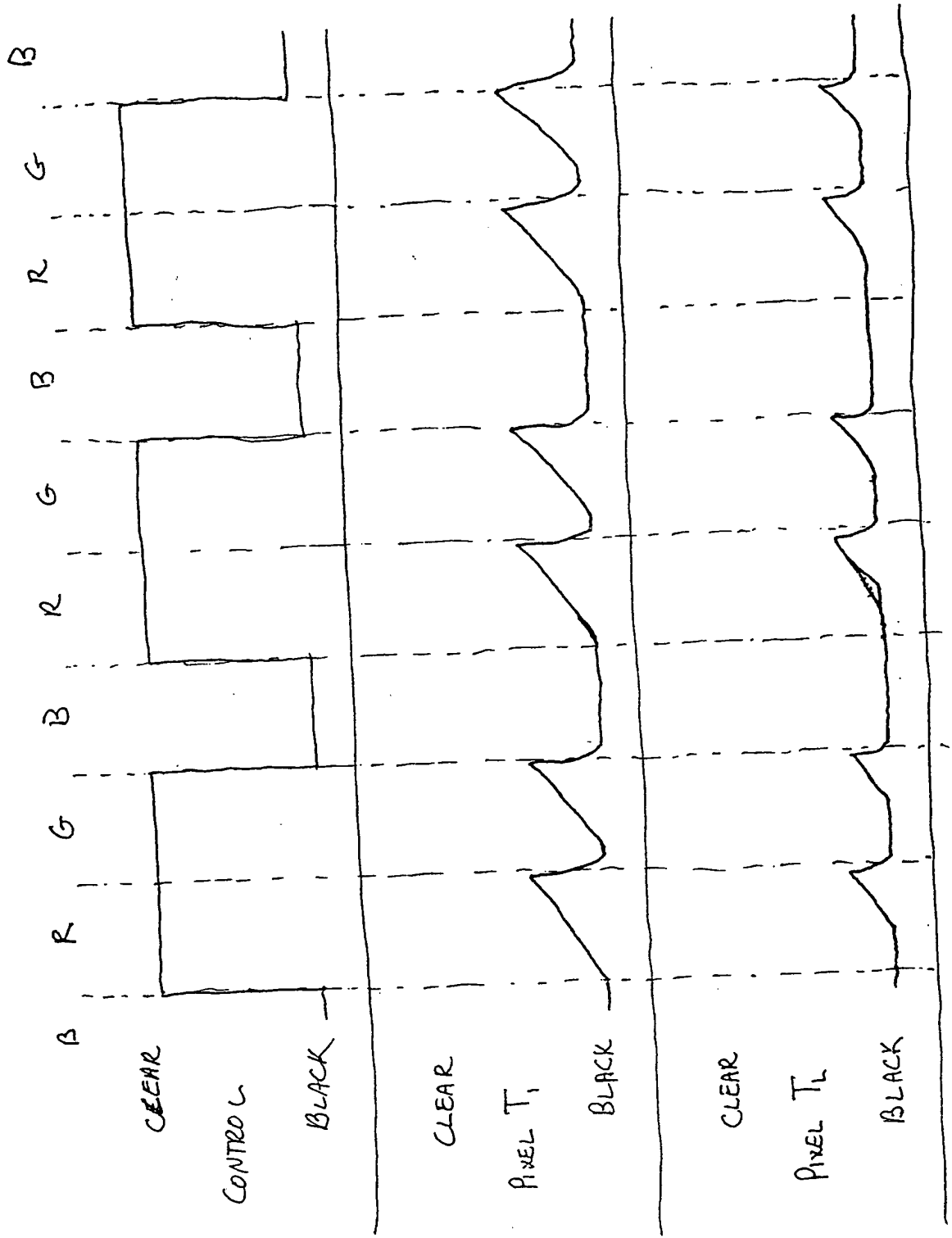


FIGURE 14

0906061-04243B

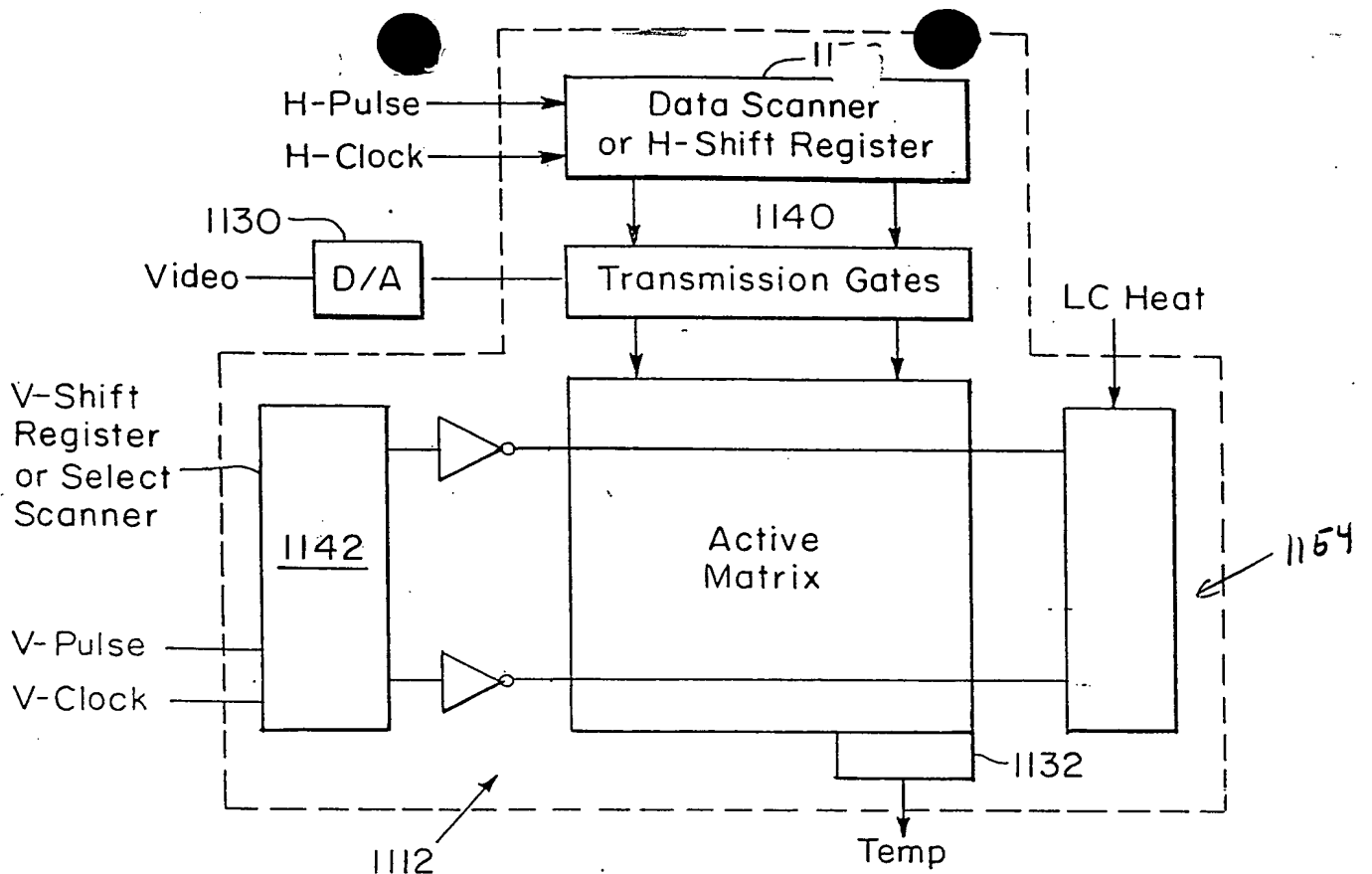


FIGURE 15A

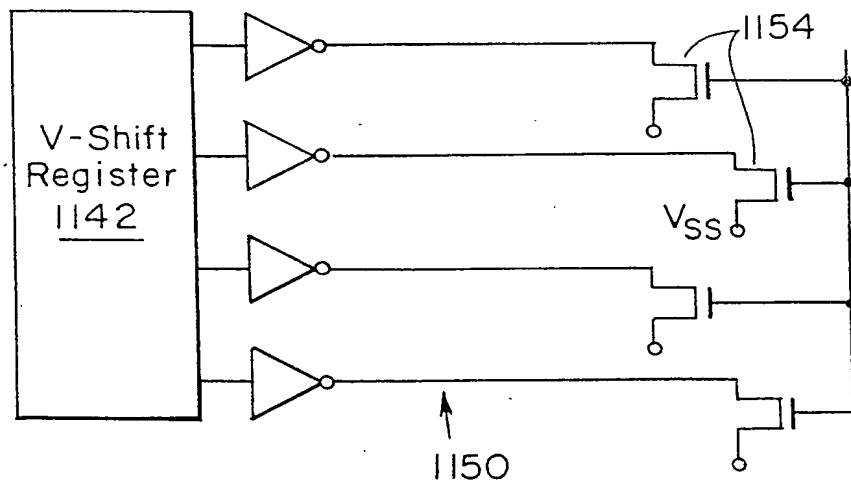


FIGURE 15B

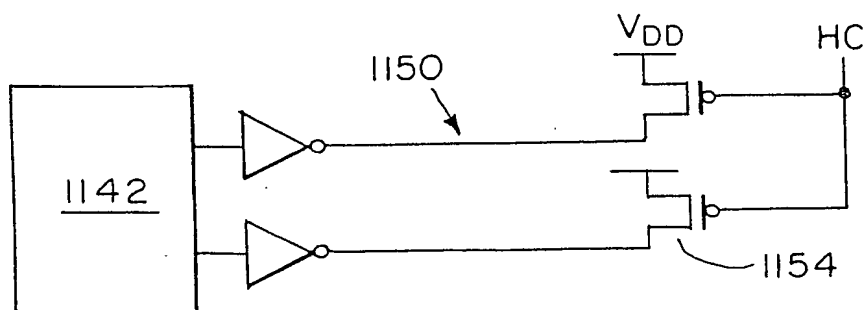


FIGURE 15C

09066061.042498

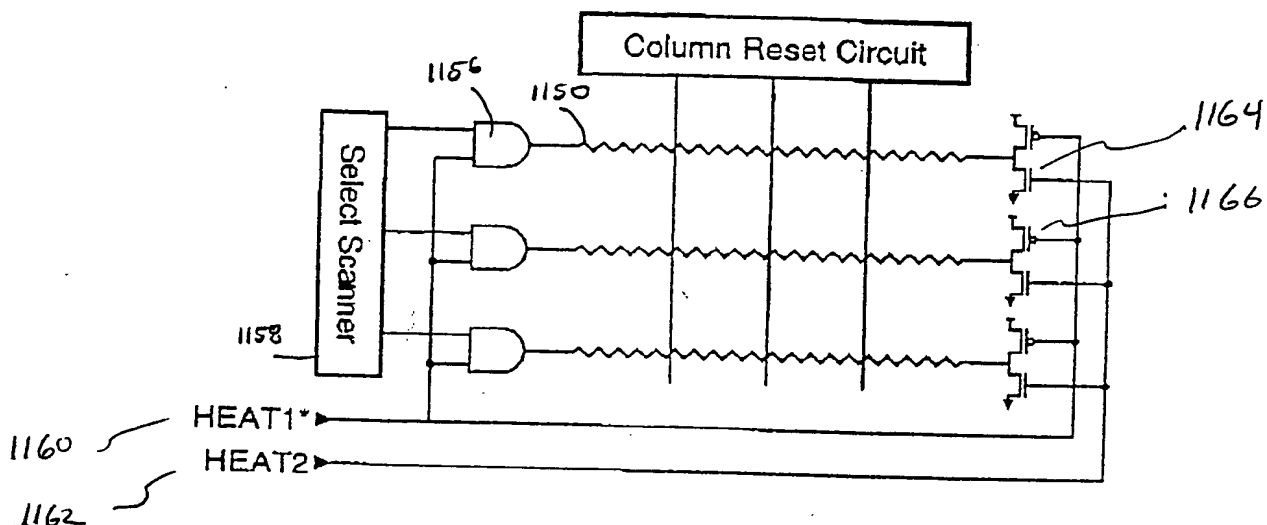


FIGURE 15D

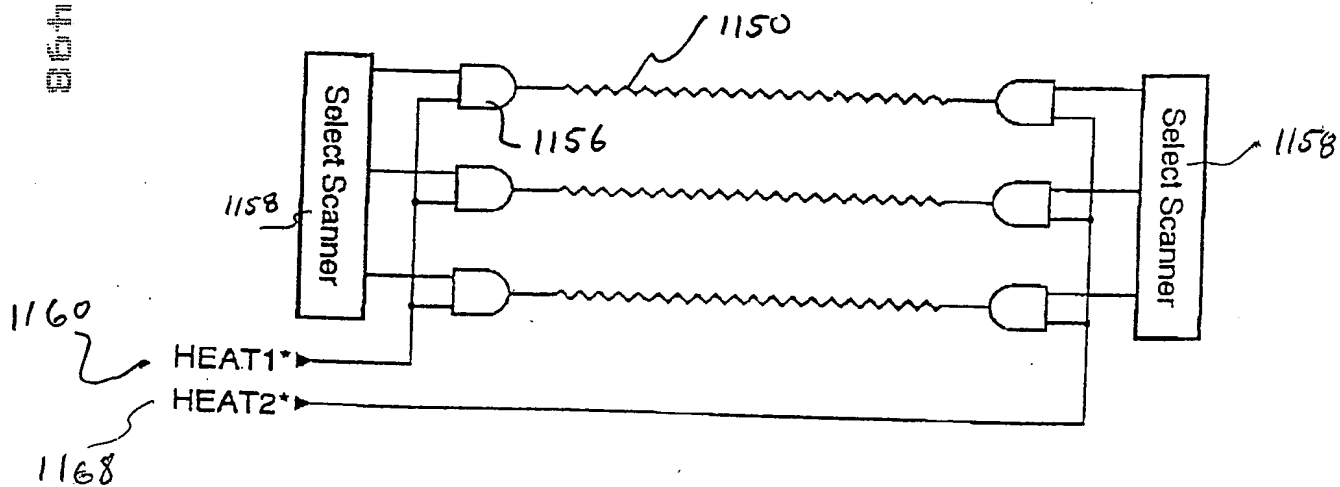


FIGURE 15E

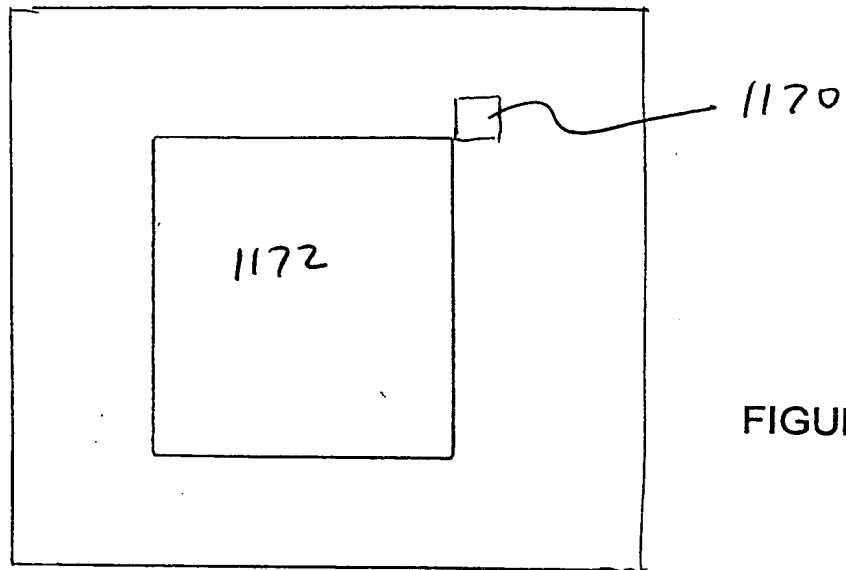


FIGURE 15F

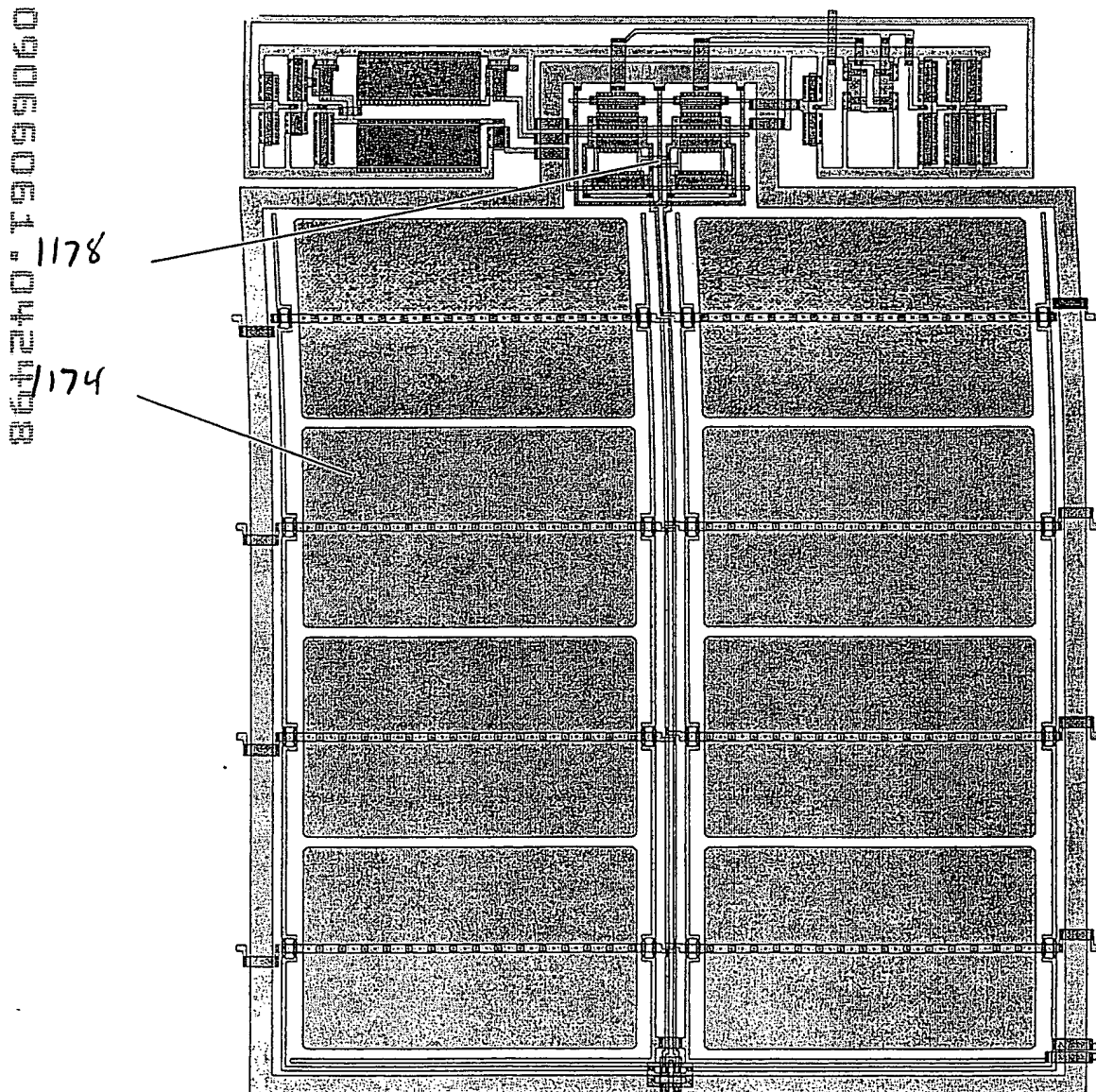


FIGURE 15G

09066061.042498

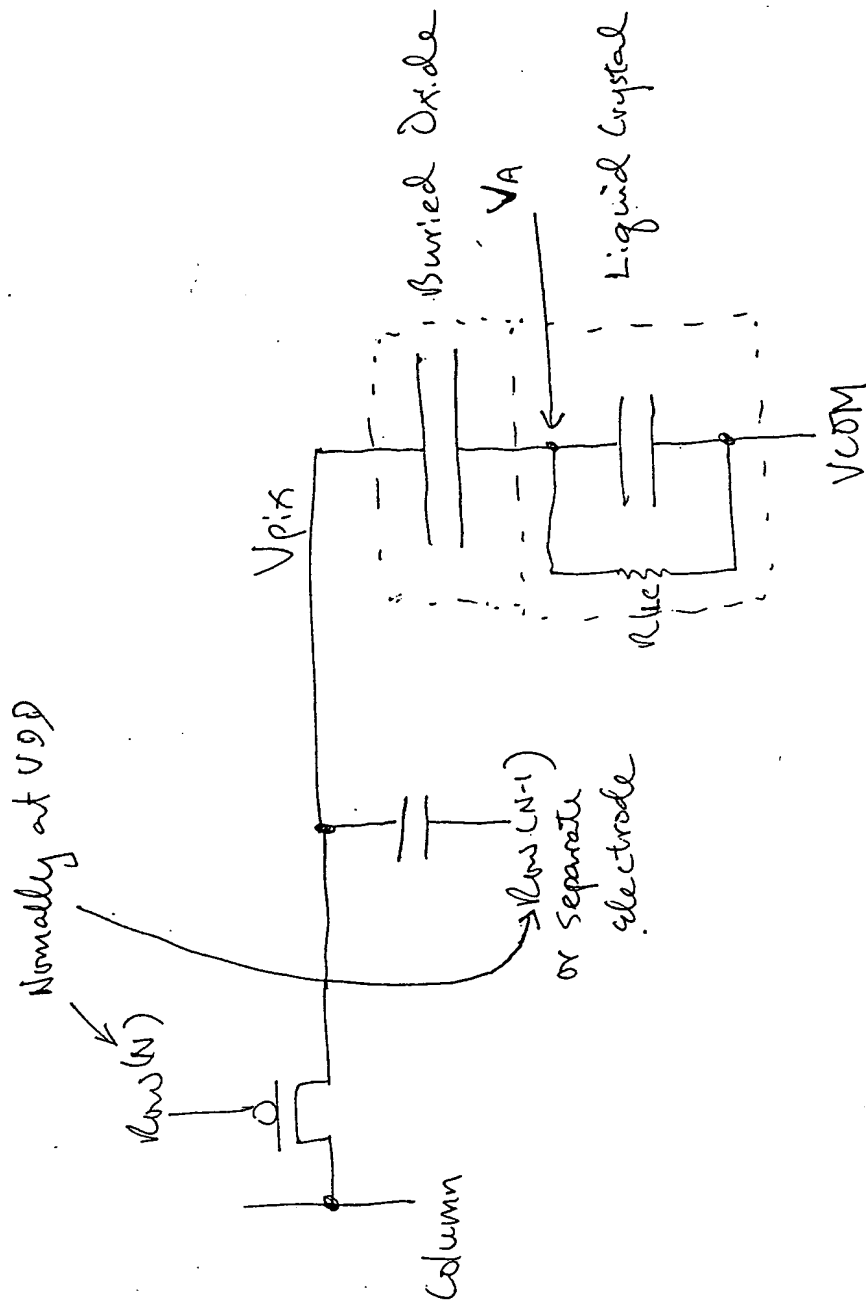
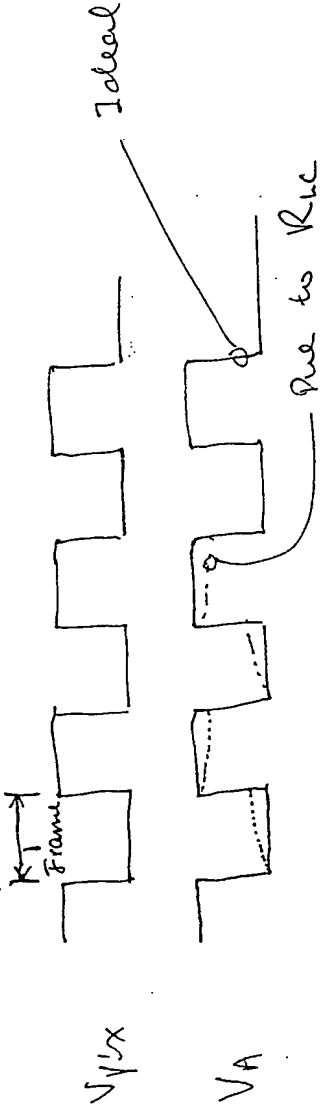
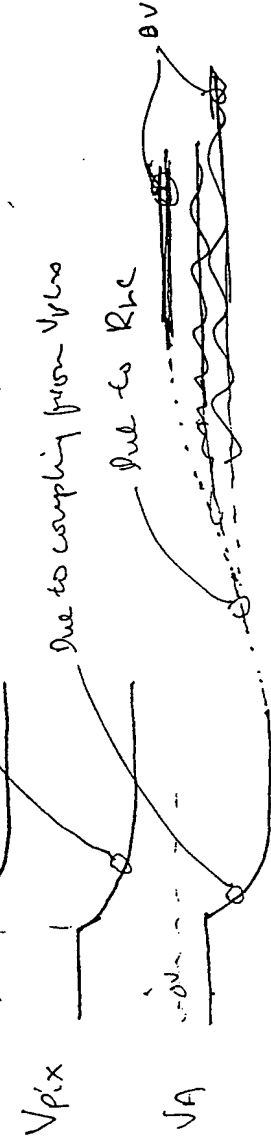


FIGURE 16

Normal Operation:



During Power Off:
Power (VDD)



Now Powers back on

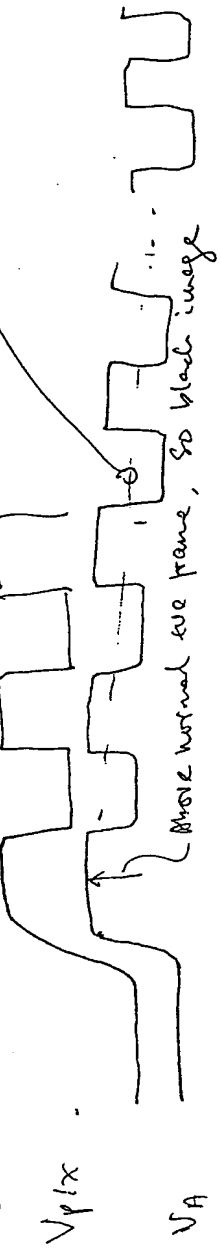


FIGURE 17

854240 "T5099060

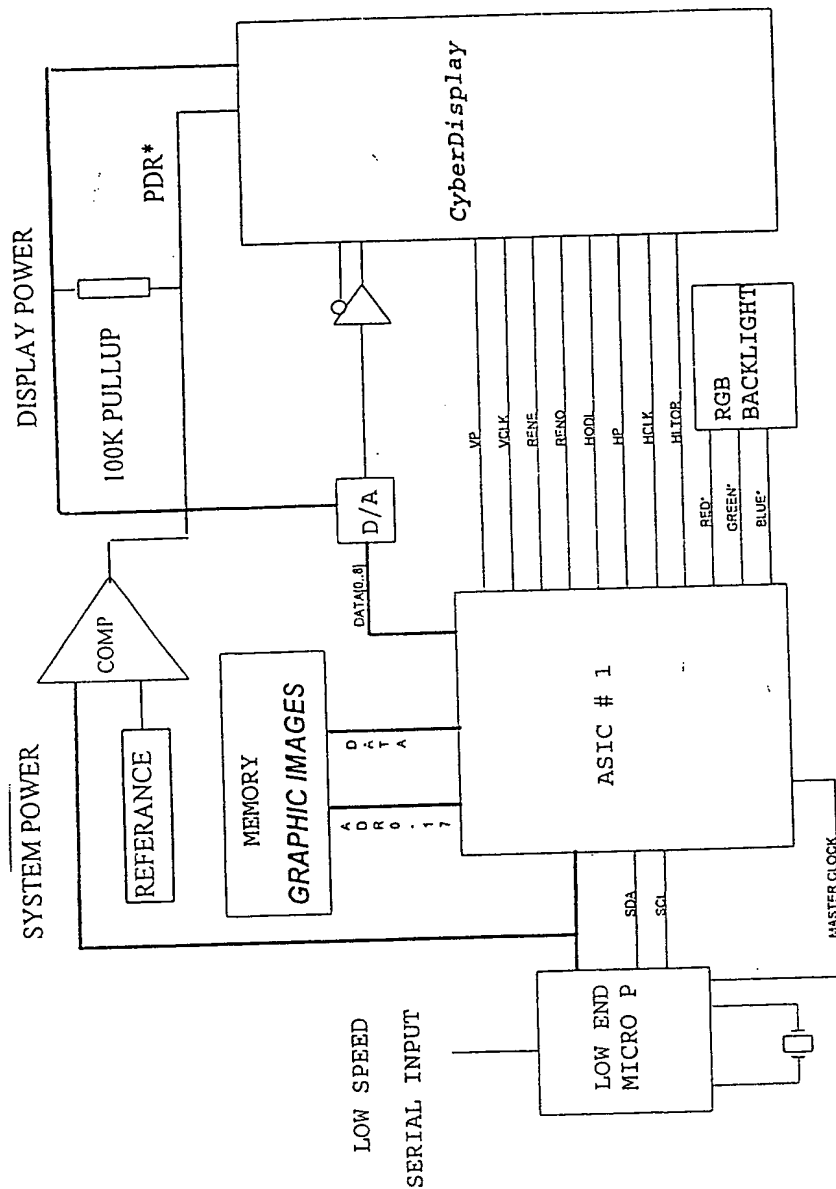


FIGURE 18

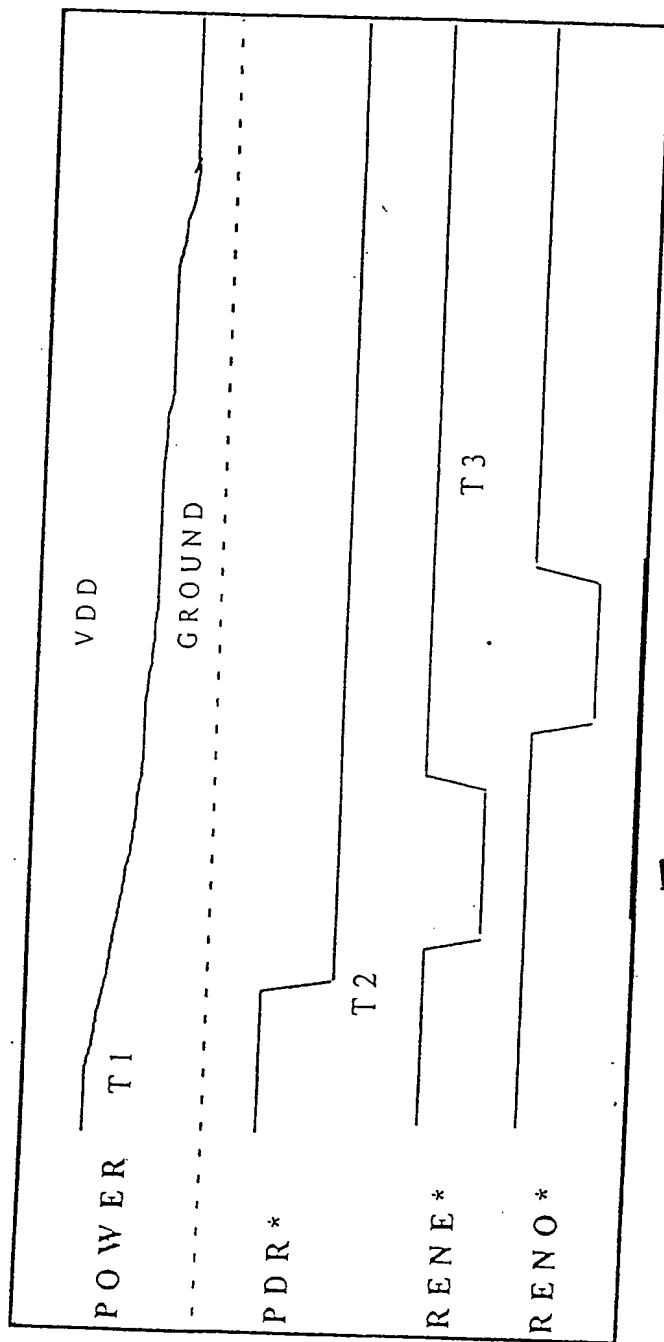


FIGURE 19

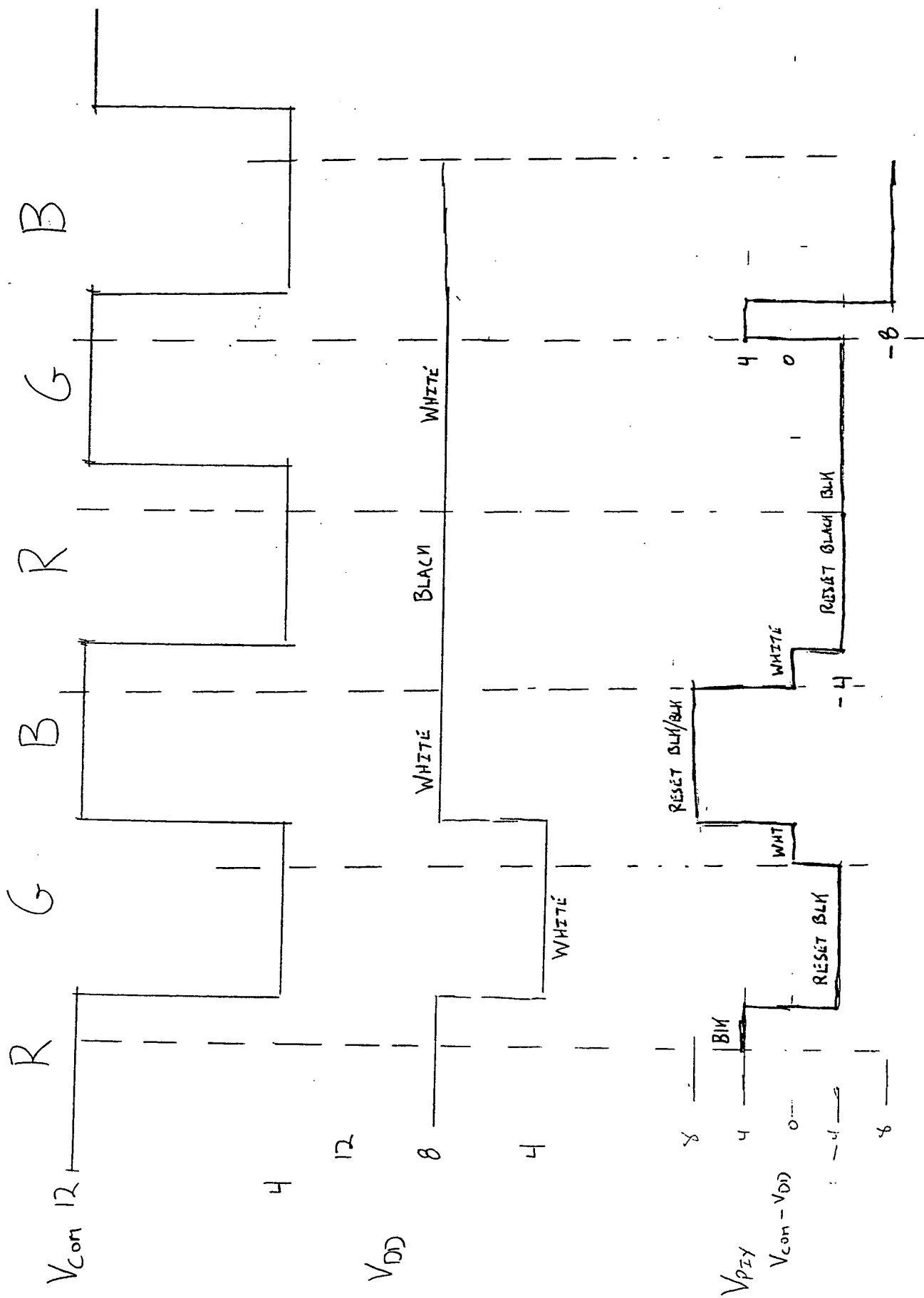


FIGURE 20

864240 T9099060

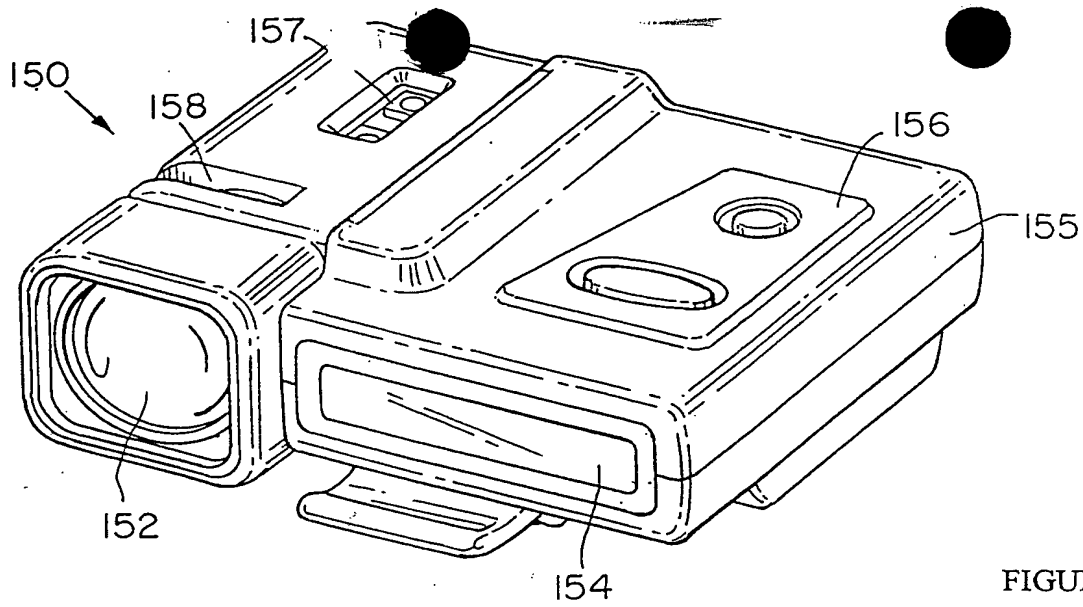


FIGURE 21C

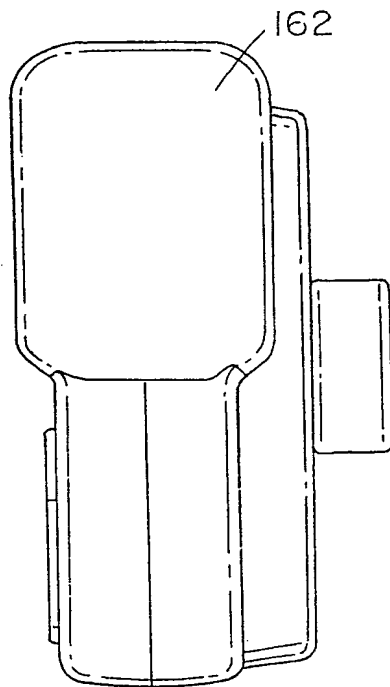
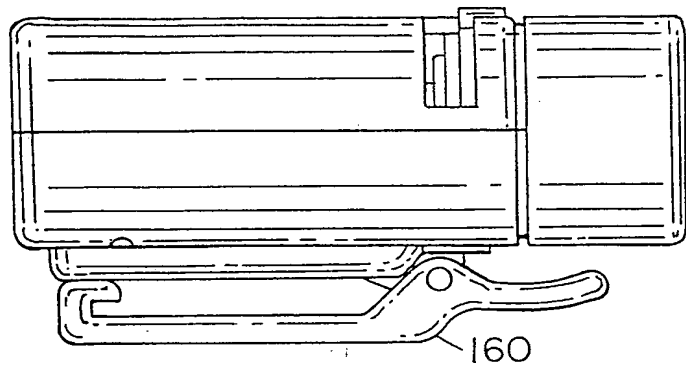


FIGURE 21B

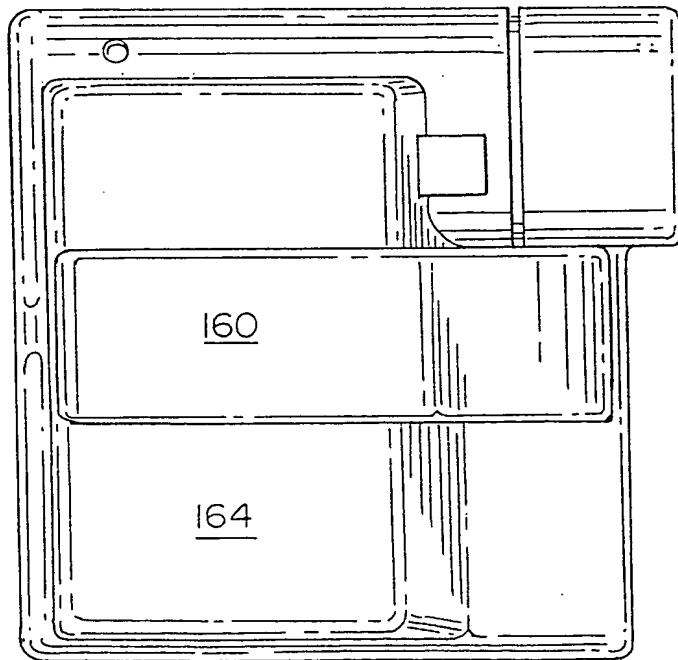


FIGURE 21D

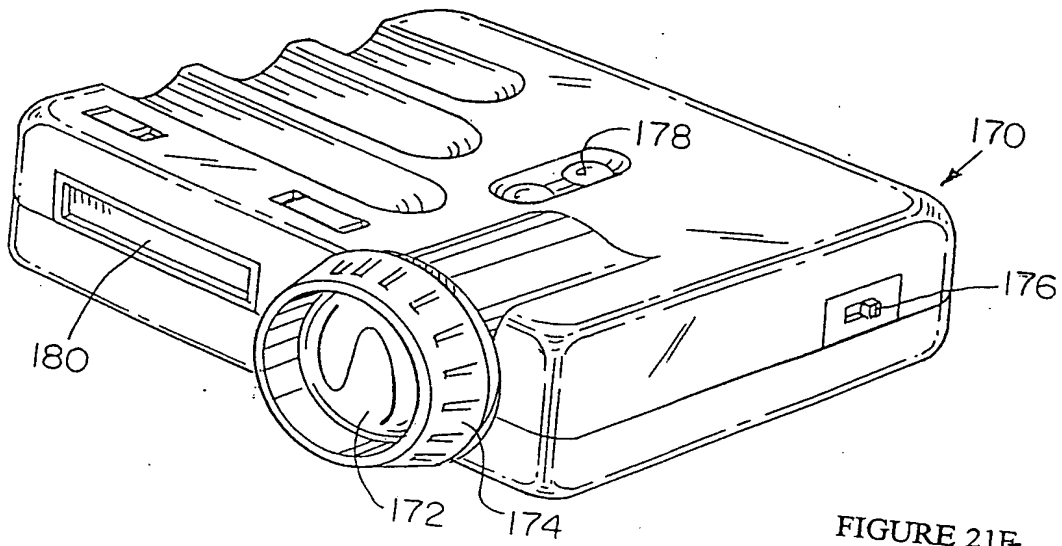


FIGURE 21E

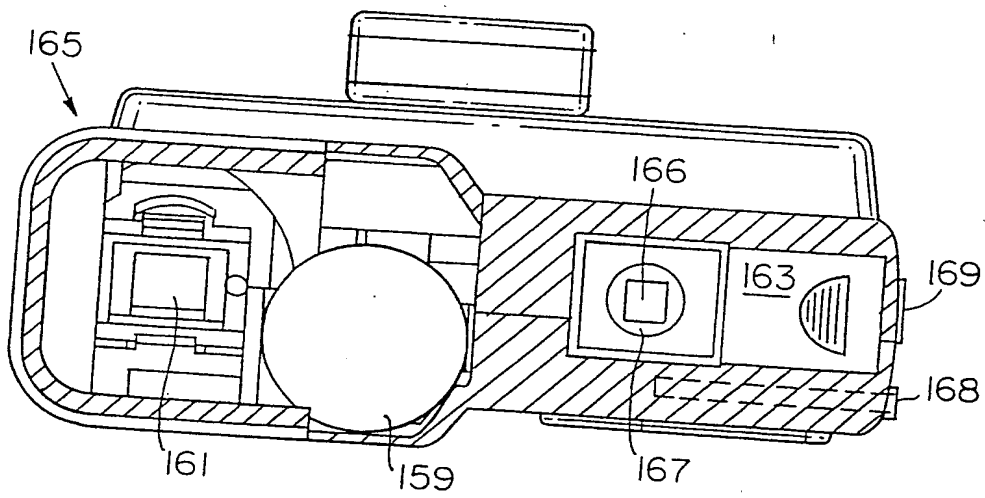


FIGURE 22

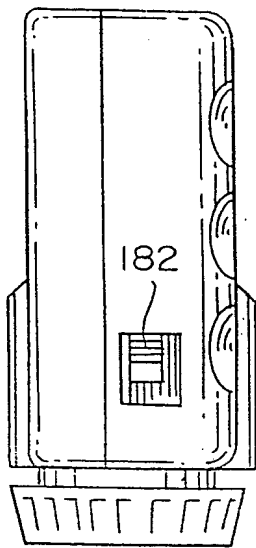


FIGURE 21G

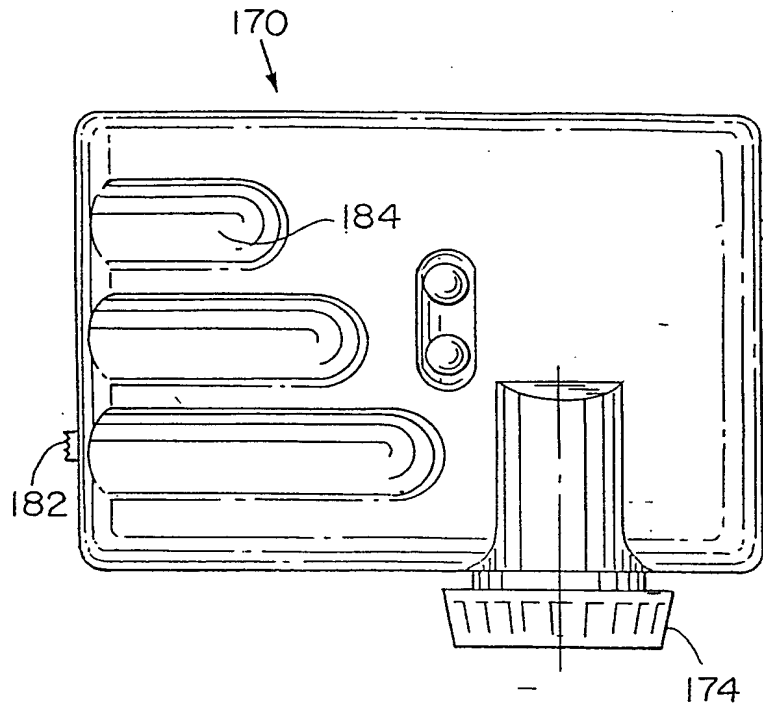


FIGURE 21F

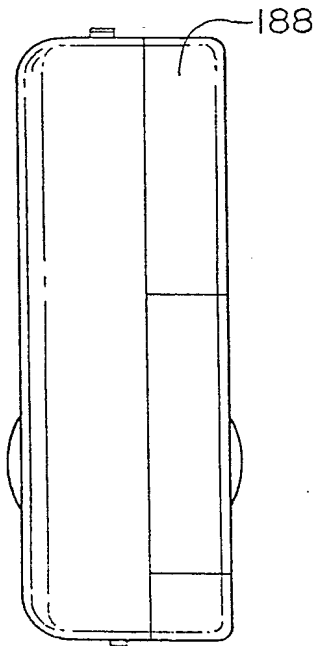


FIGURE 21H

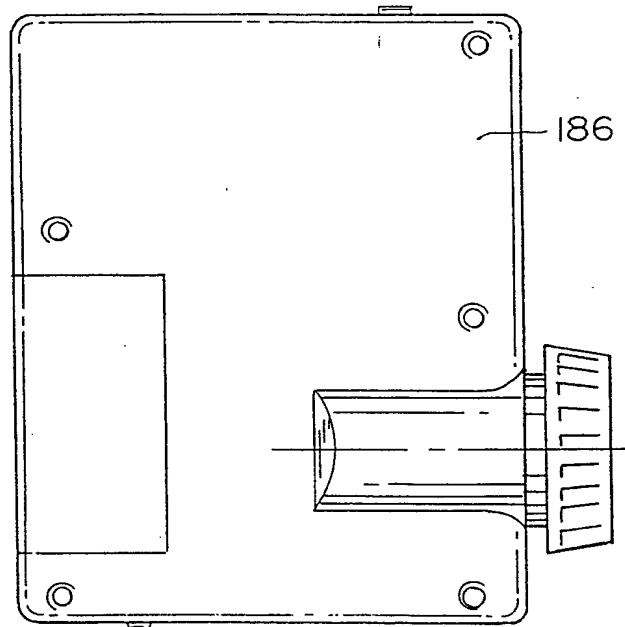


FIGURE 21I

09066061.04498

FIGURE 21J

190

192

191

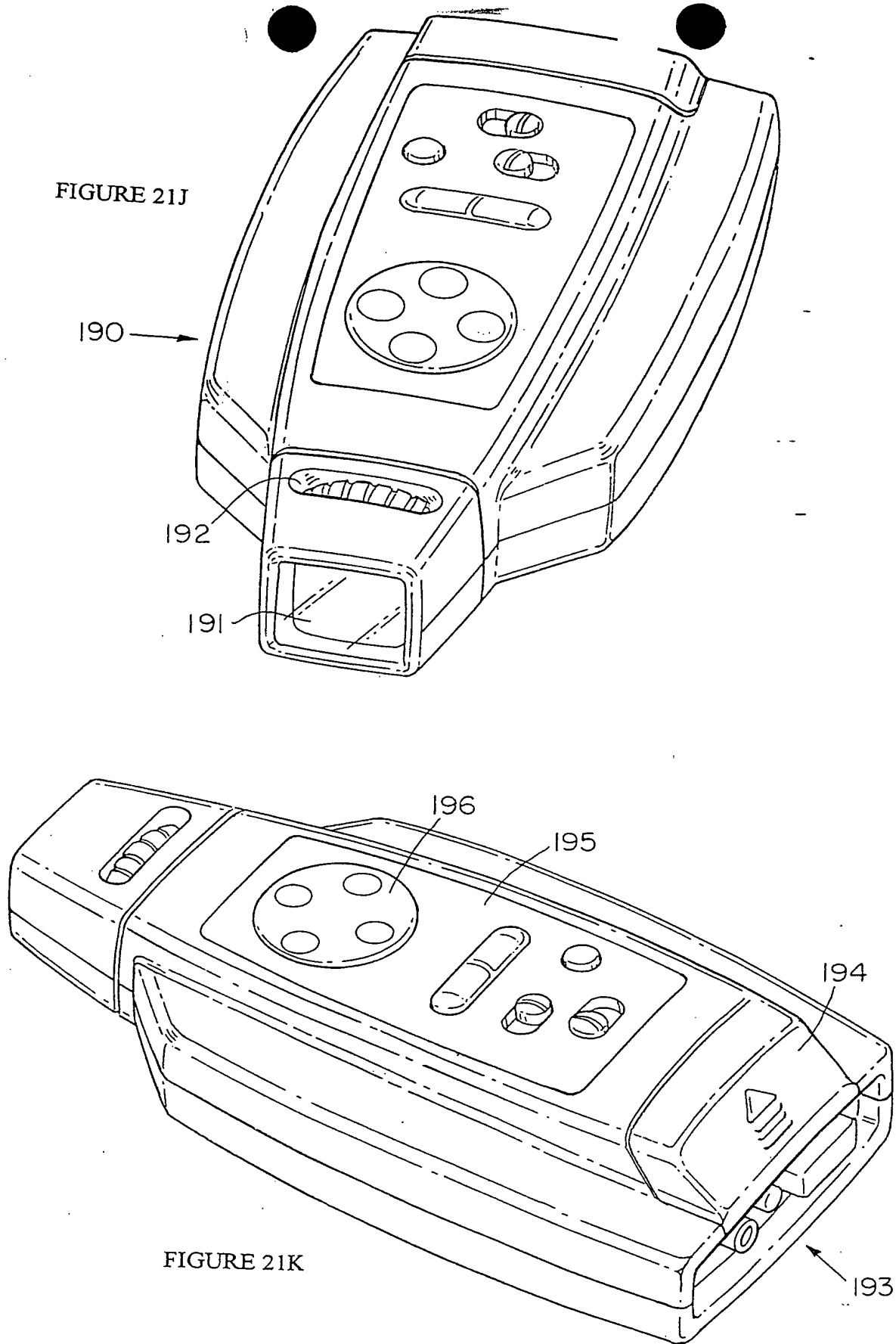
FIGURE 21K

196

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193



09066061.04498
864240" T9099060

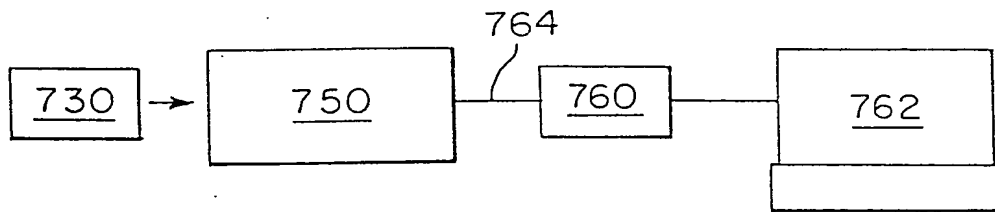


FIGURE 23A

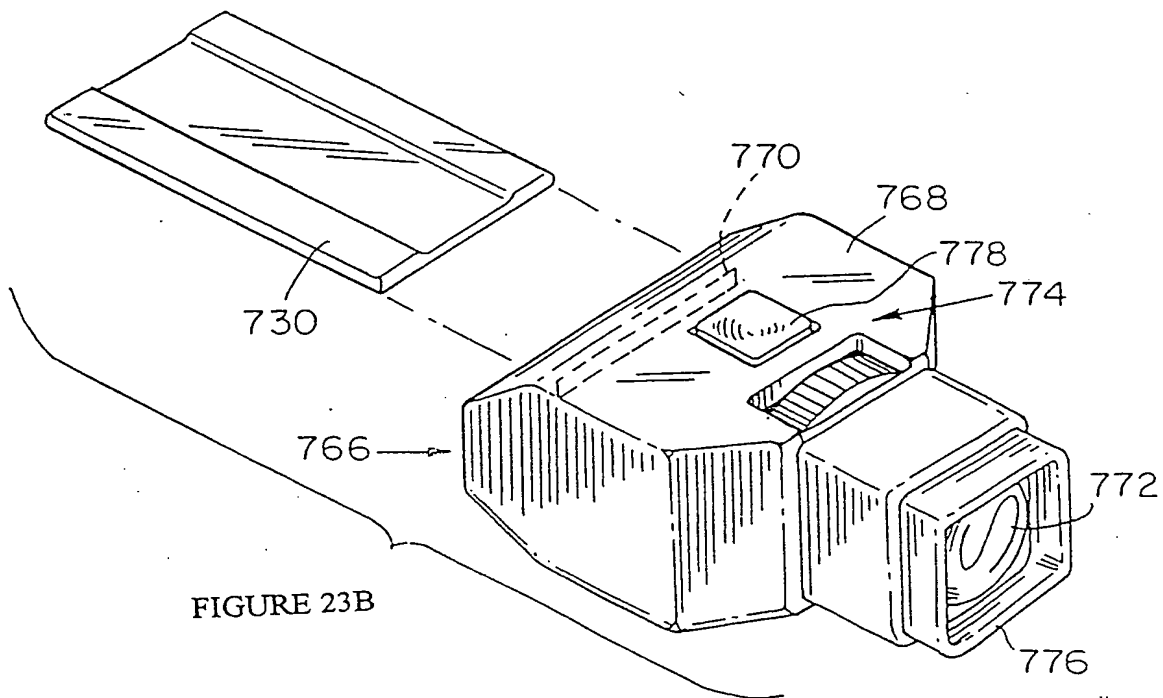


FIGURE 23B

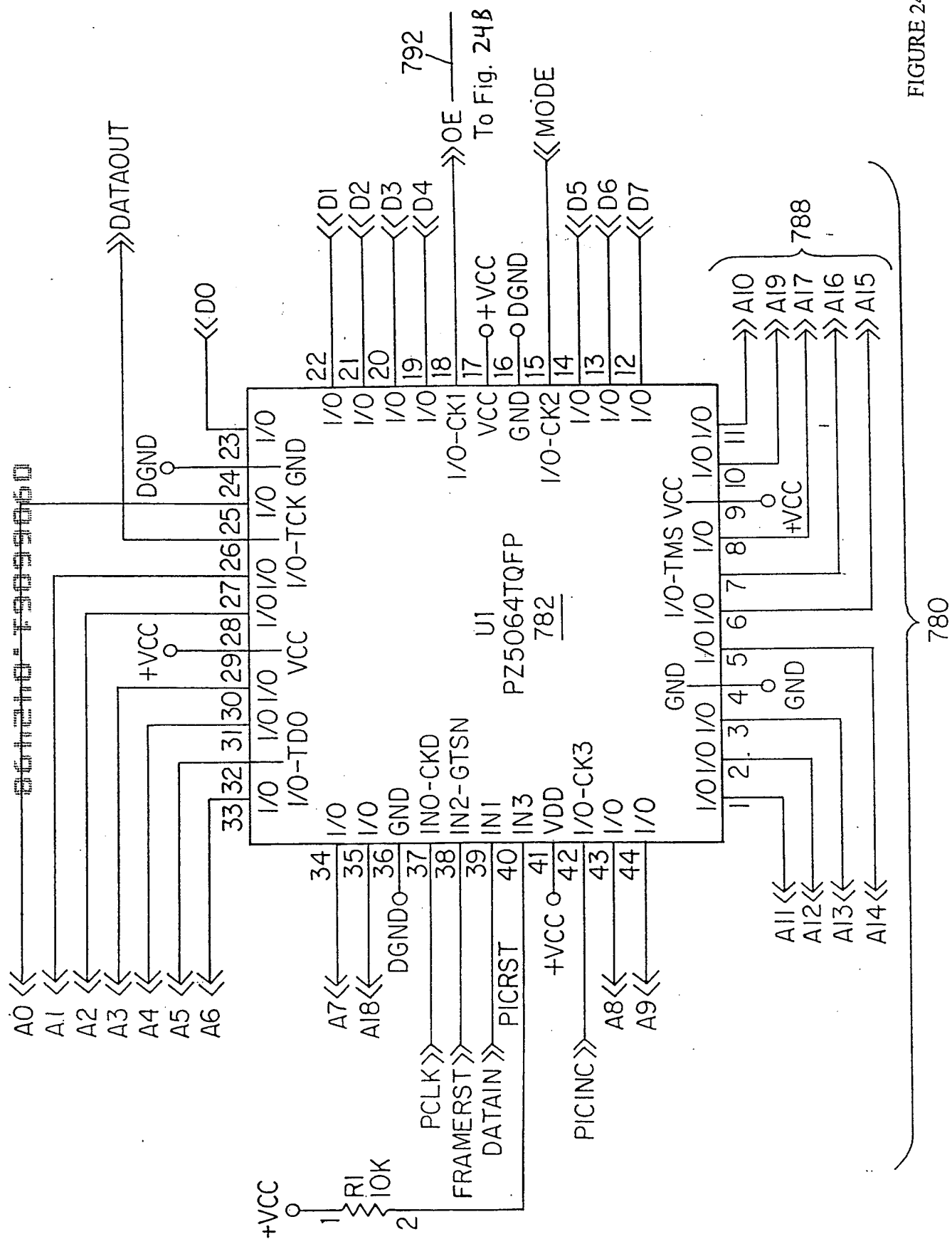
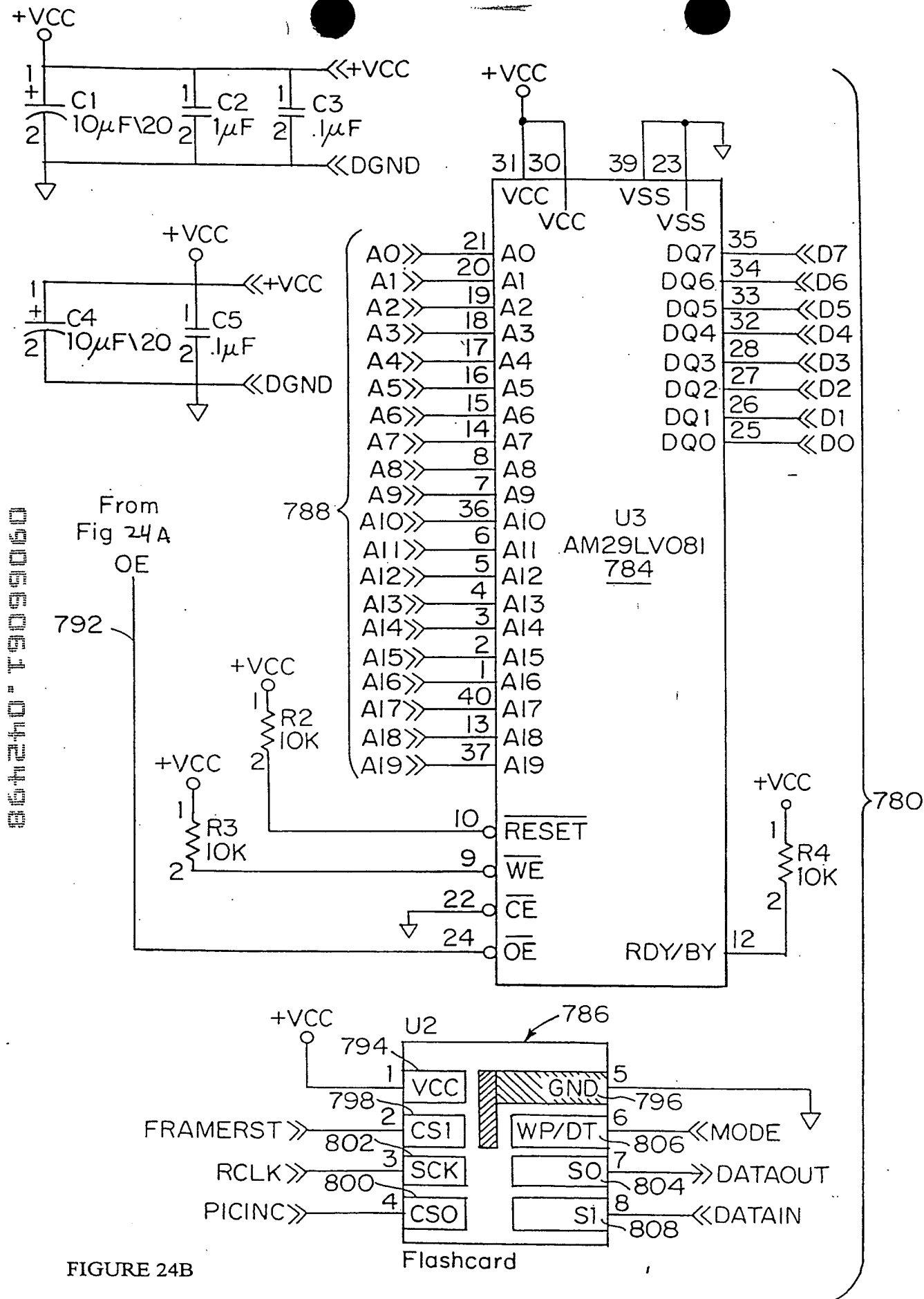


FIGURE 24A



864240-T9093060

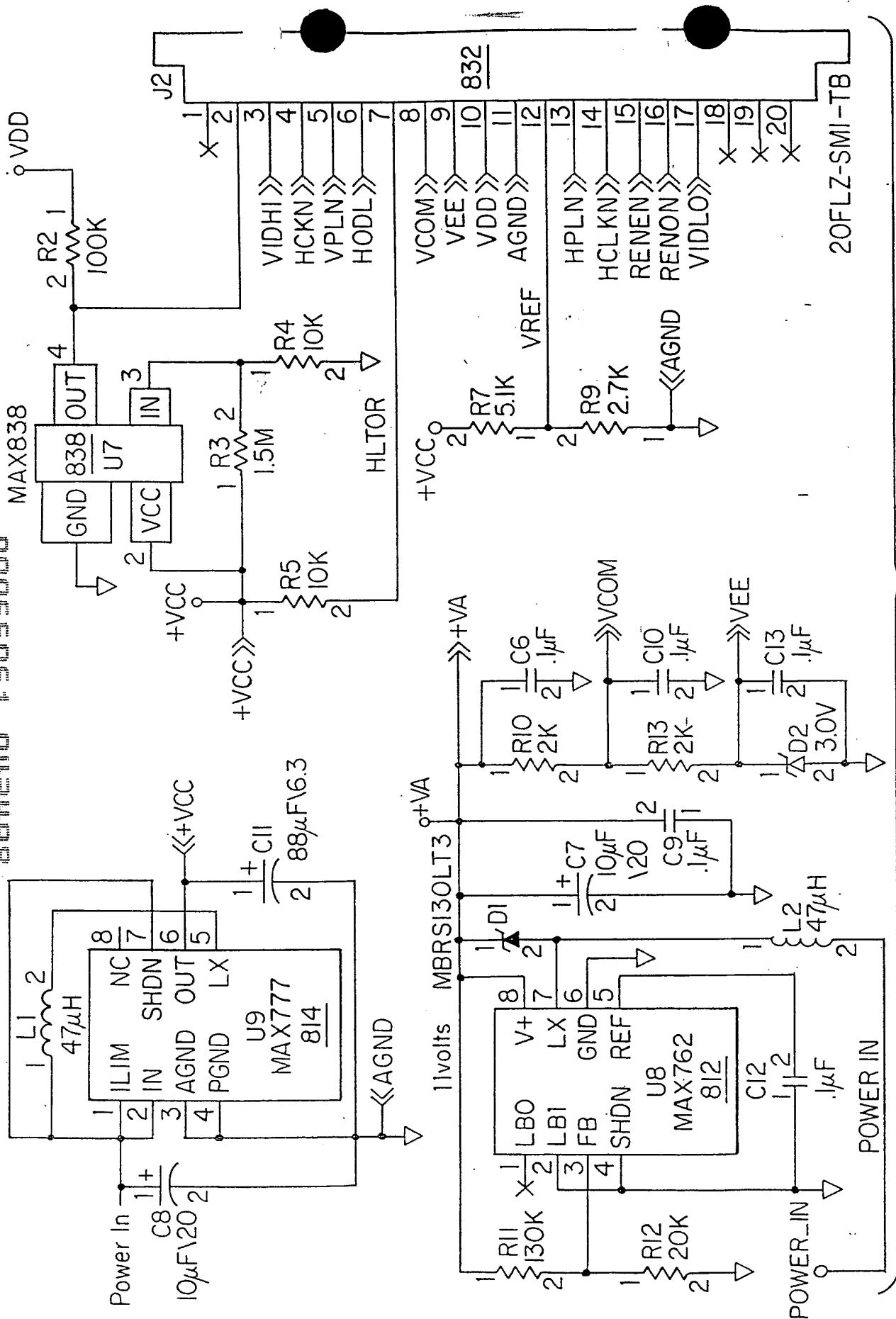


FIGURE 25A

864240-19099060

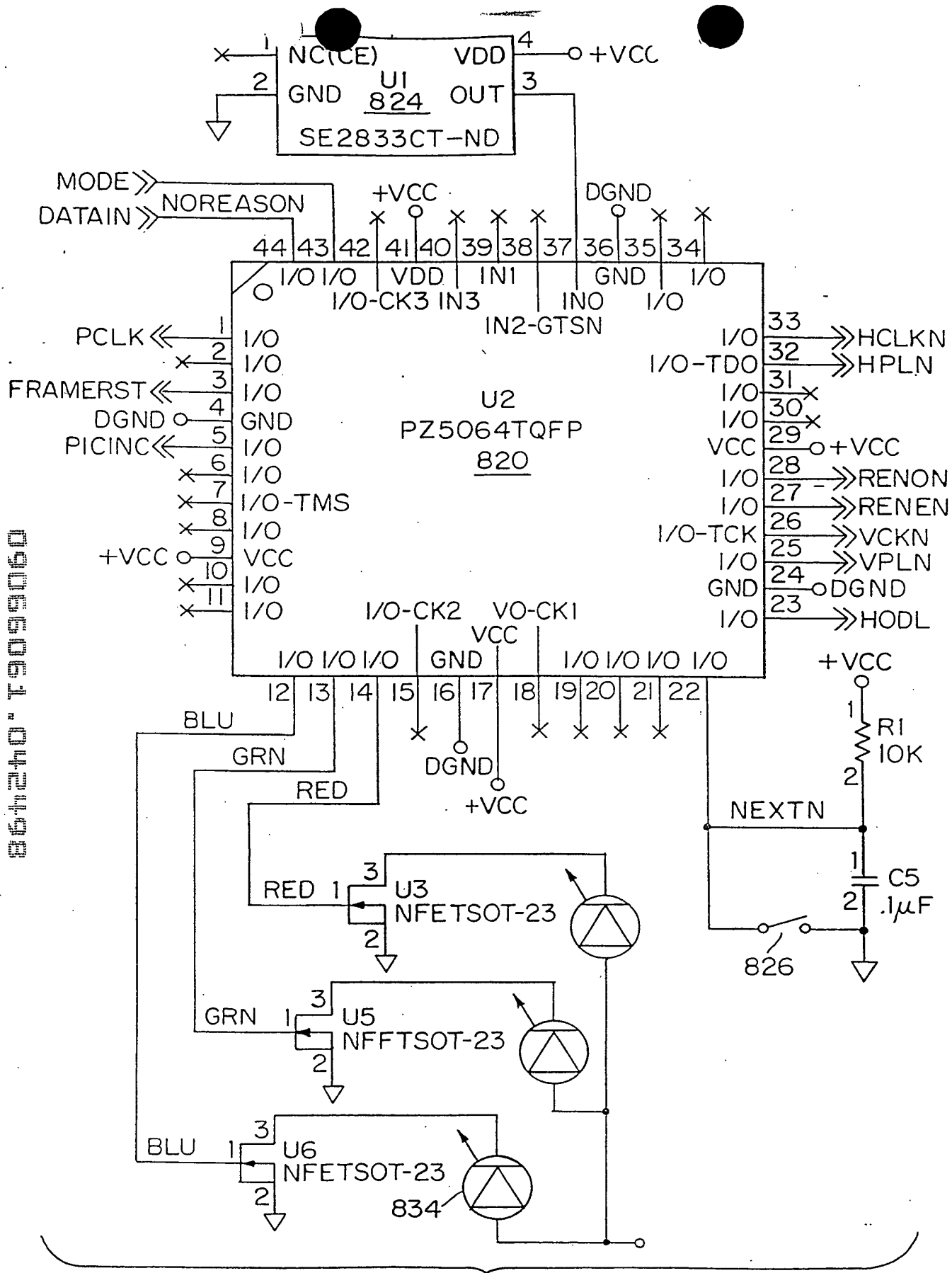


FIGURE 25B

09066061 044408

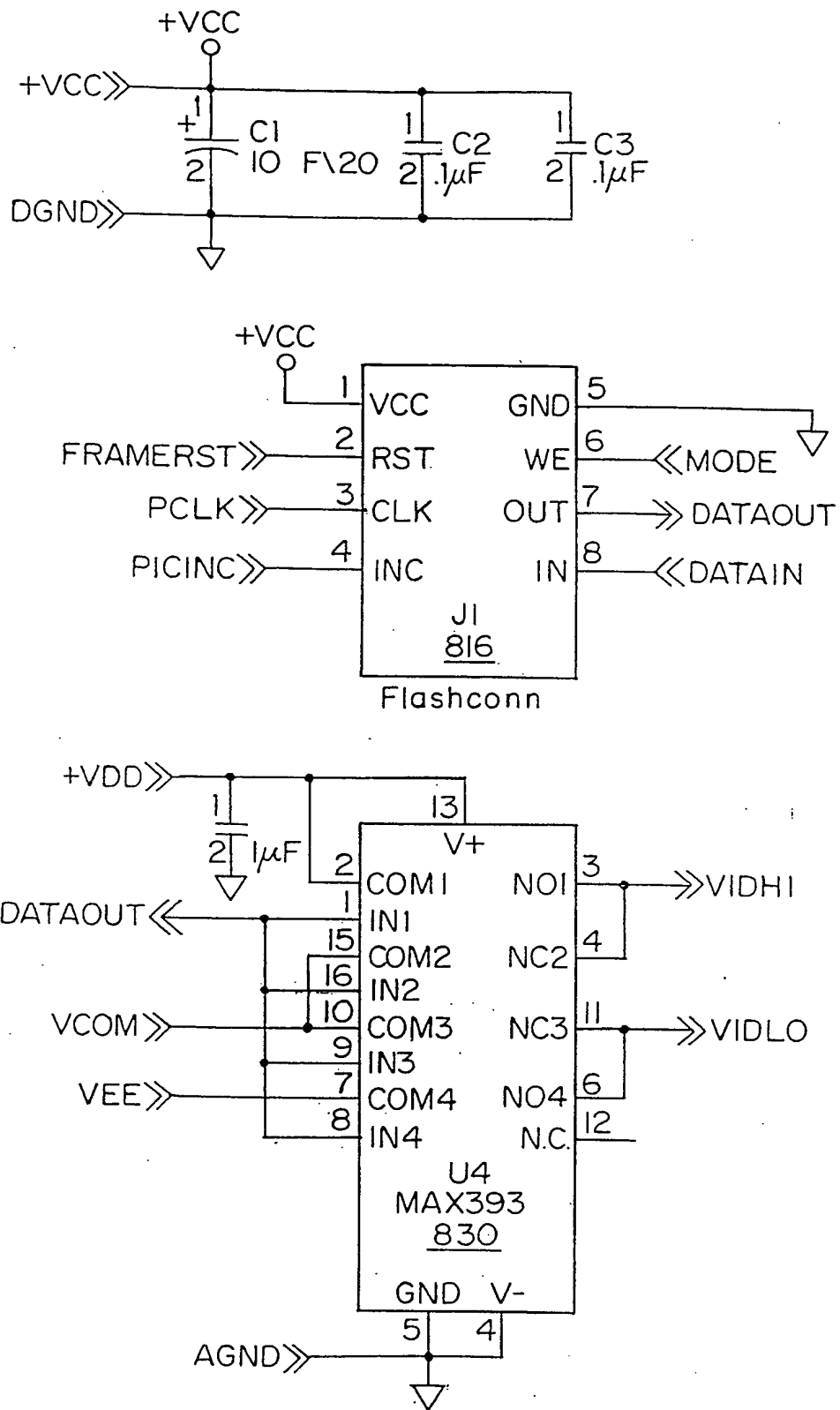


FIGURE 25C

0066061-0449

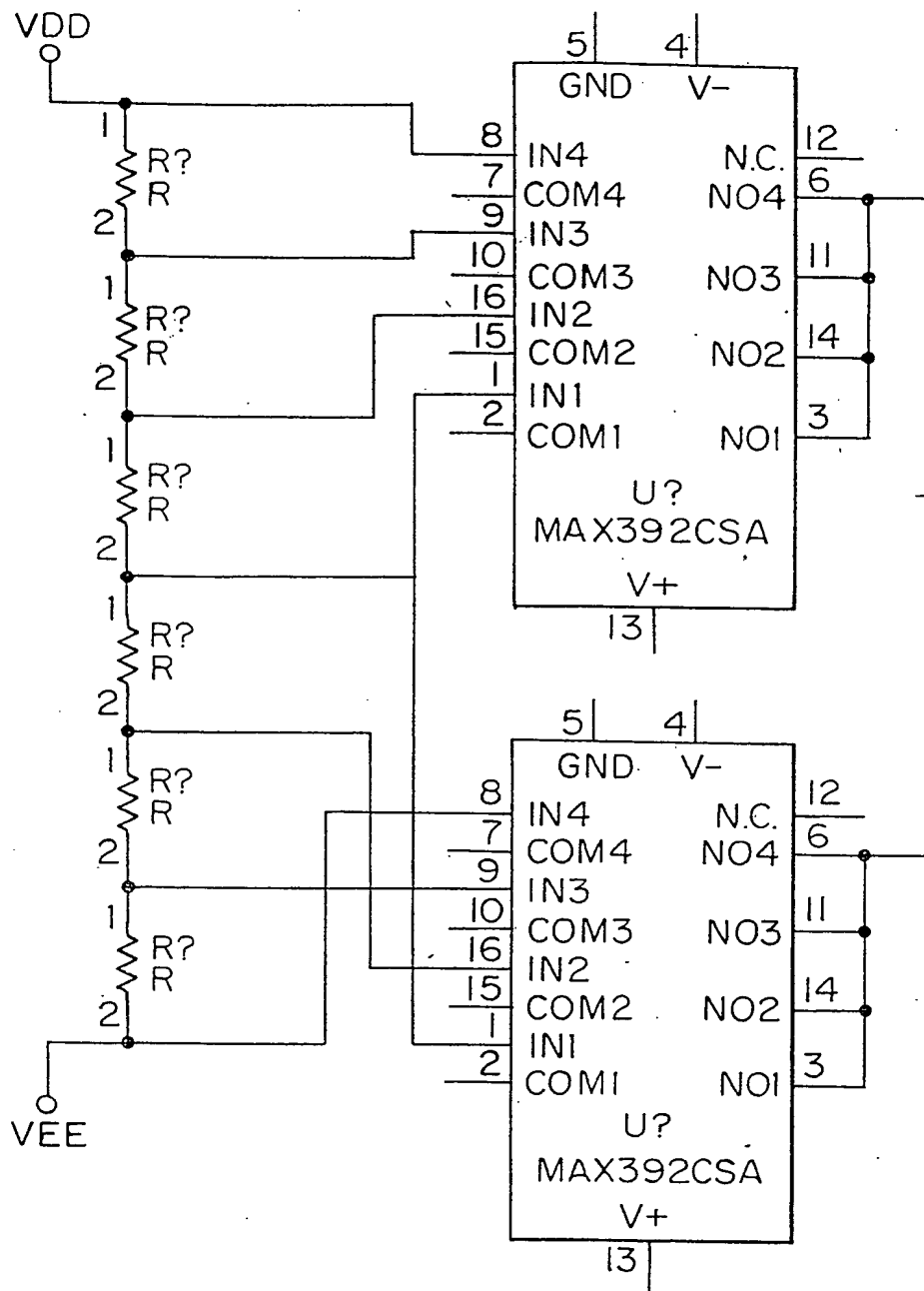
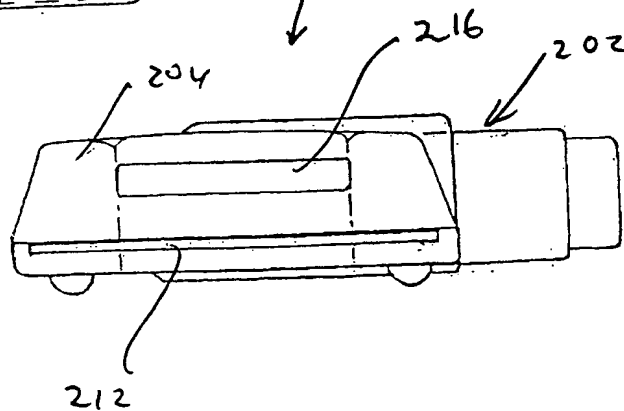
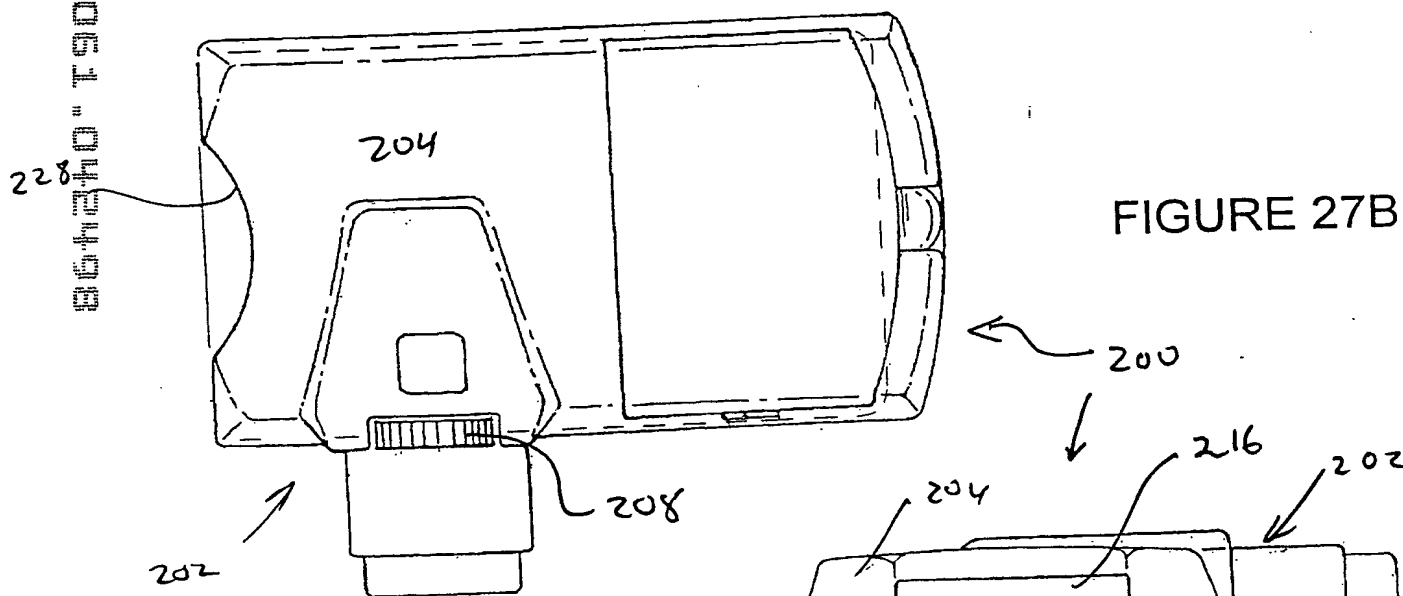
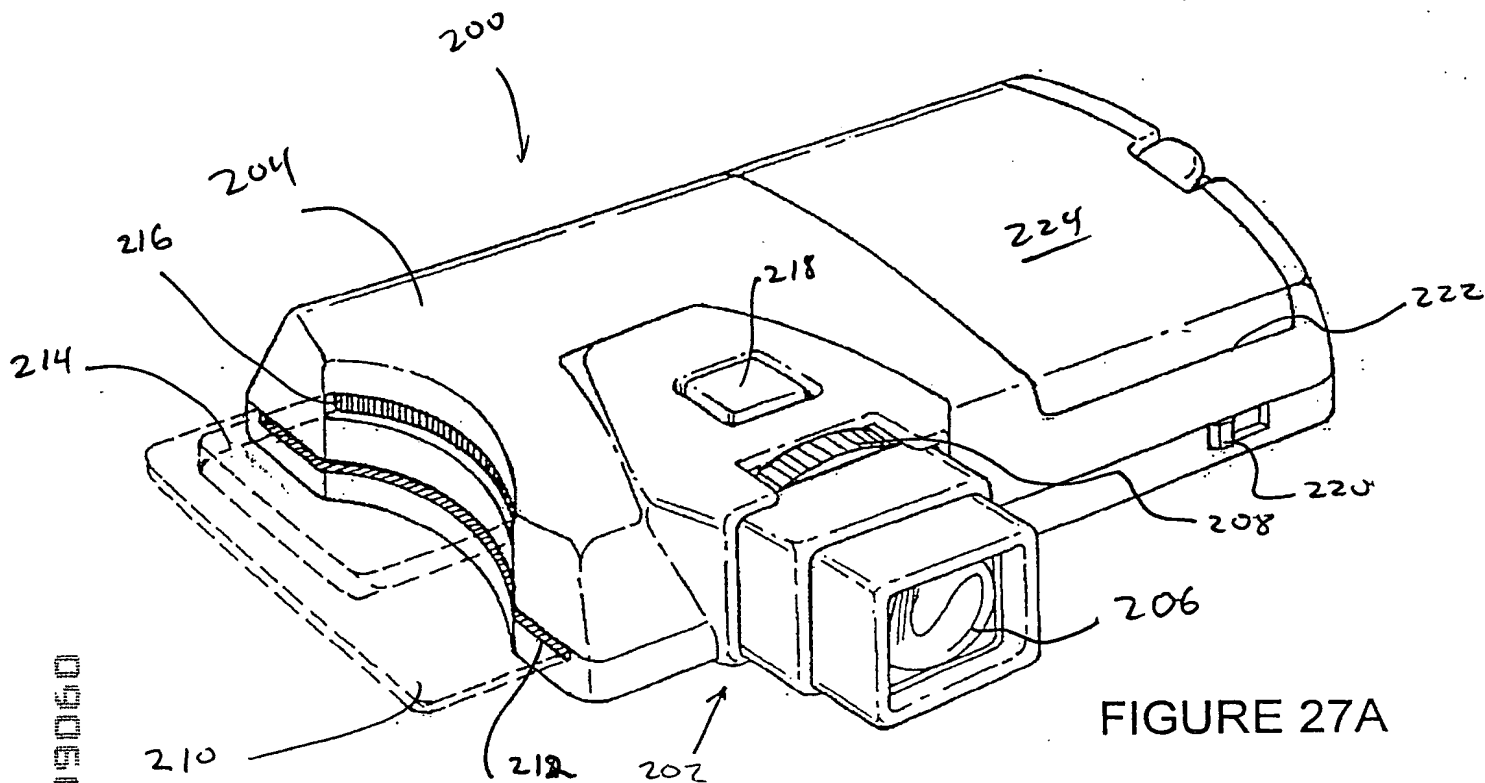


FIGURE 26



09066051-042498

09065061.042498

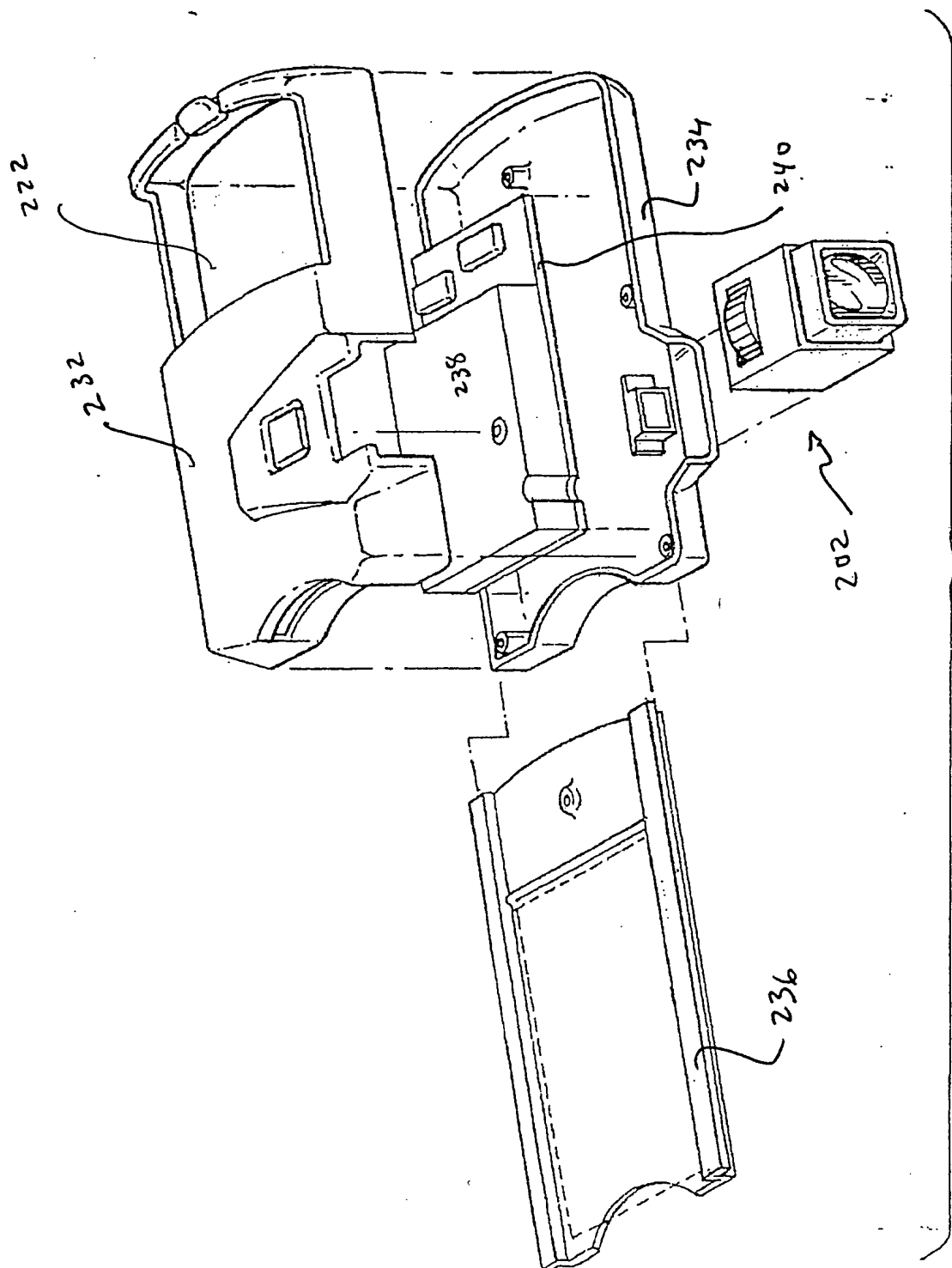


FIGURE 27D

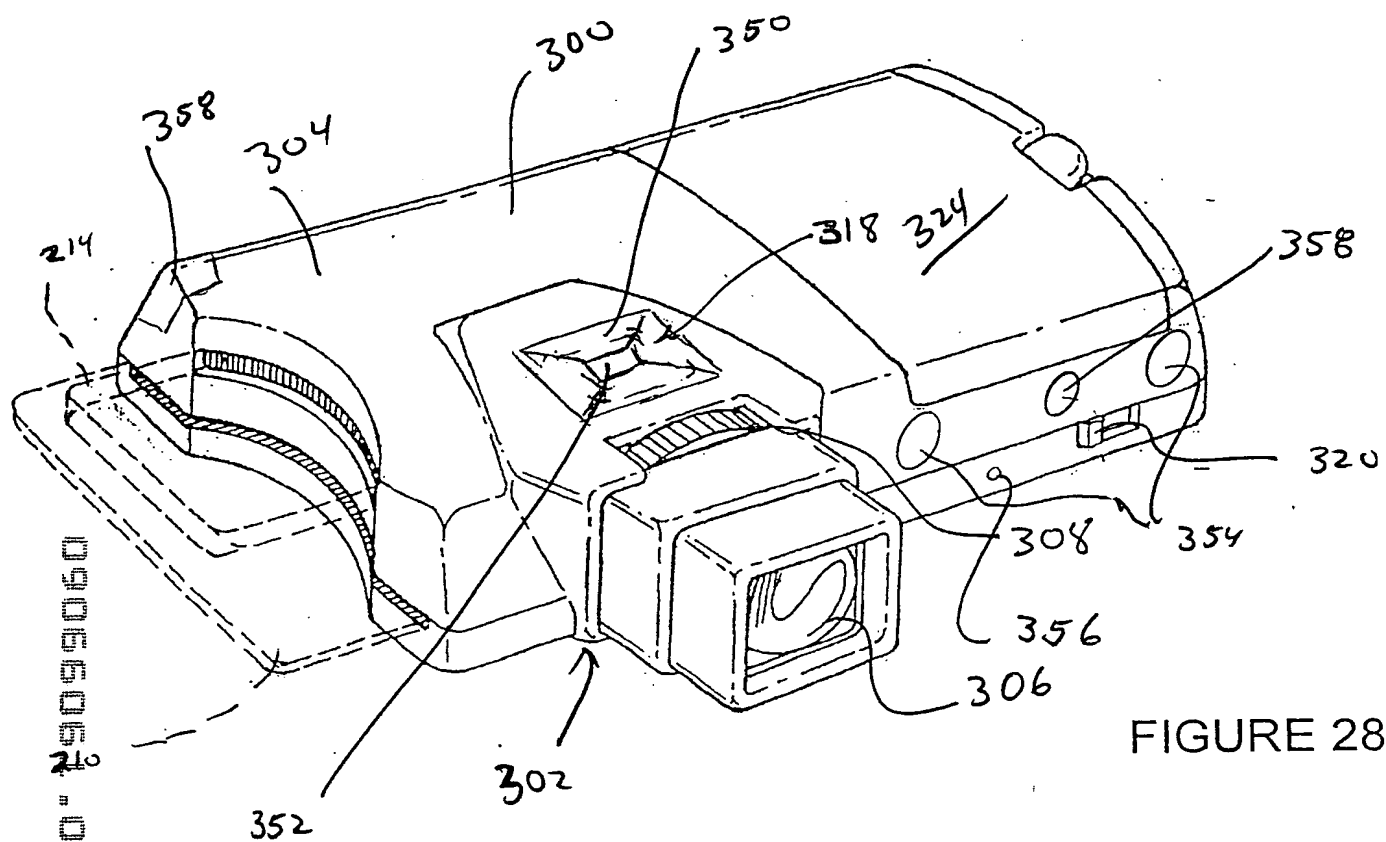


FIGURE 28A

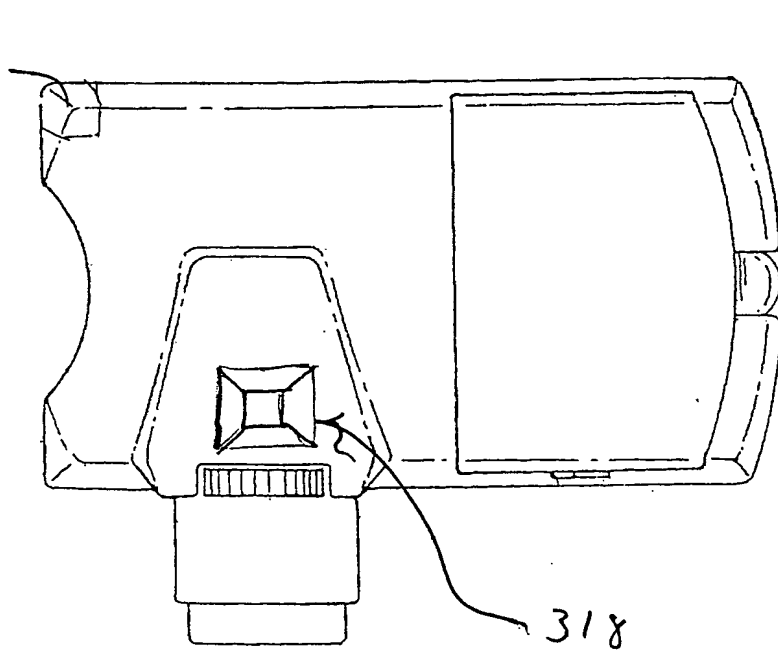


FIGURE 28B

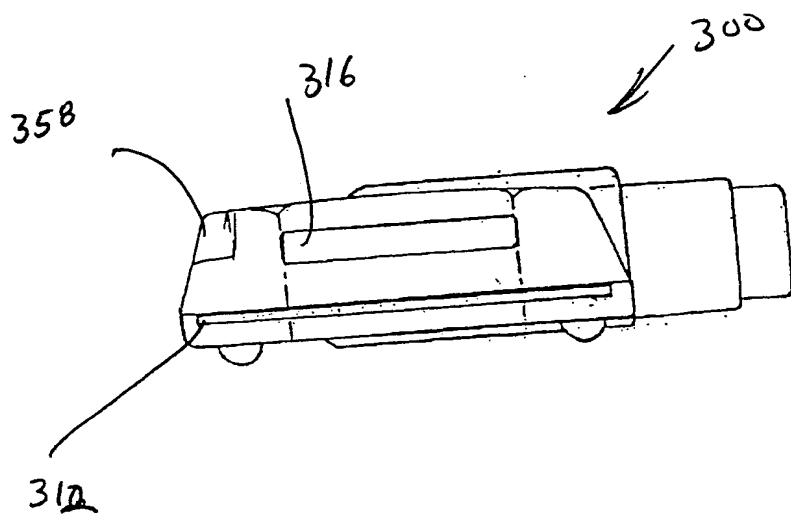


FIGURE 28C

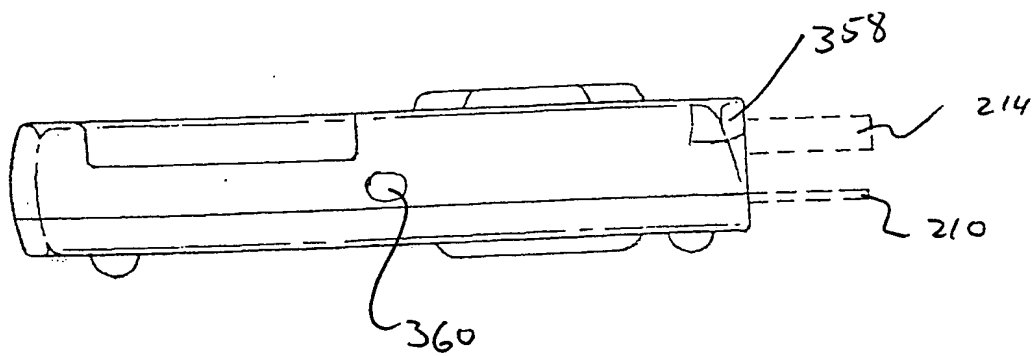


FIGURE 28D

FIGURE 29Aa

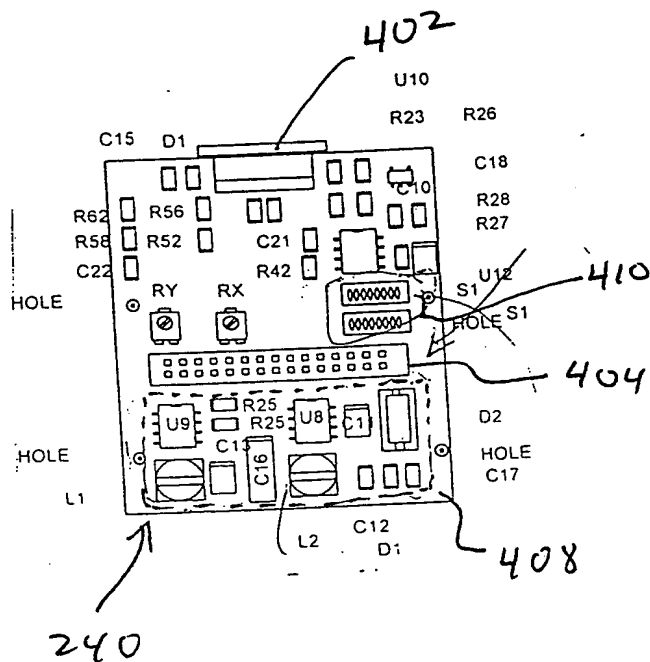


FIGURE 29Ab

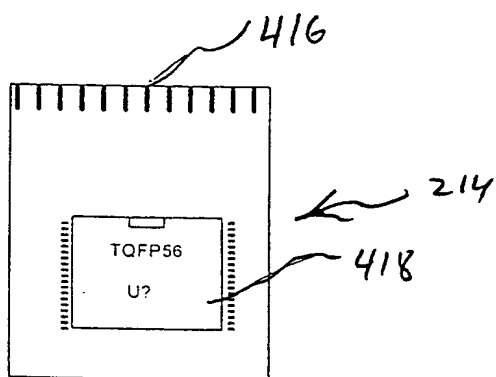
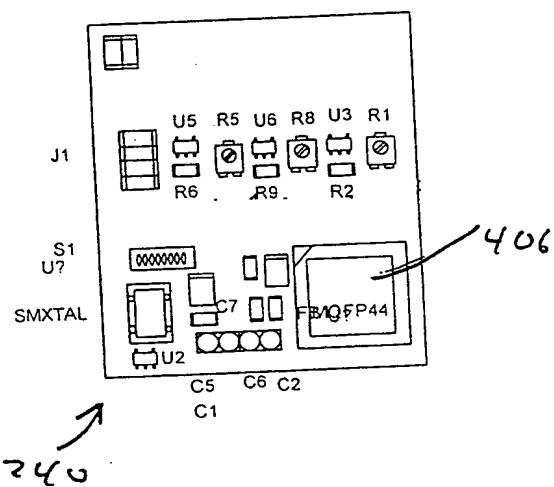


FIGURE 29Ba

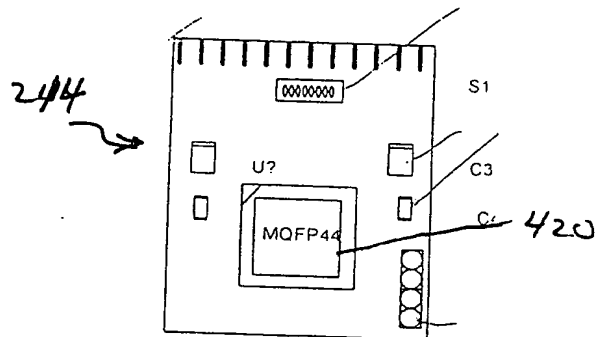


FIGURE 29Bb

09065061 0424498

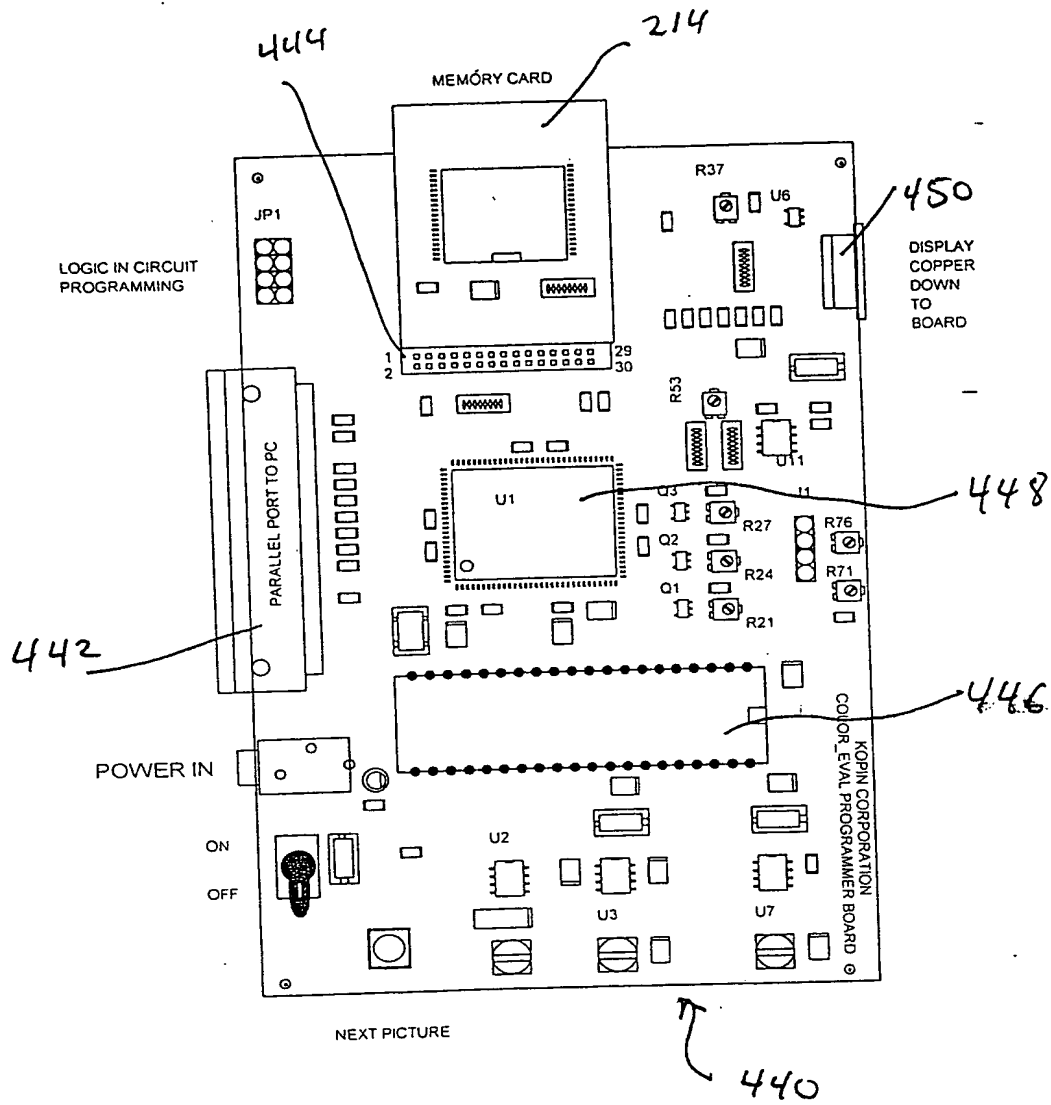


FIGURE 29C

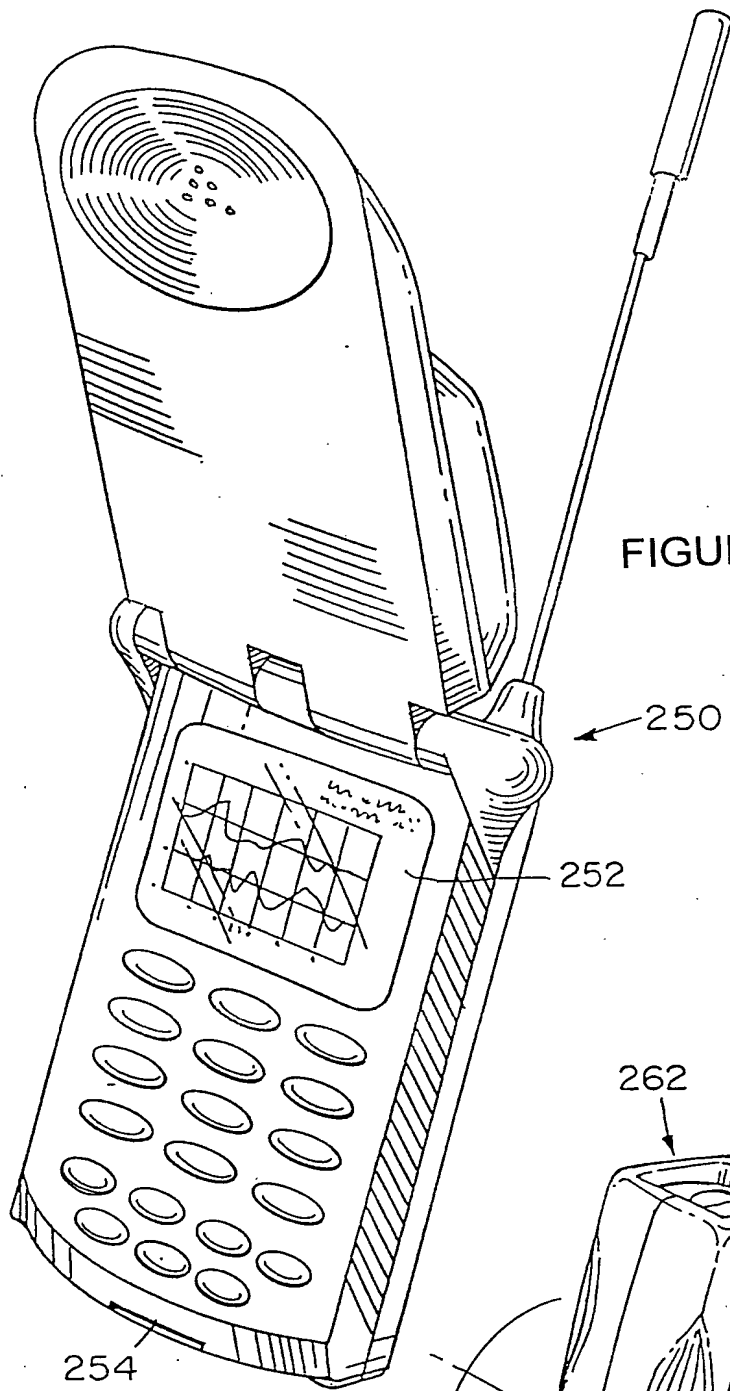


FIGURE 30A

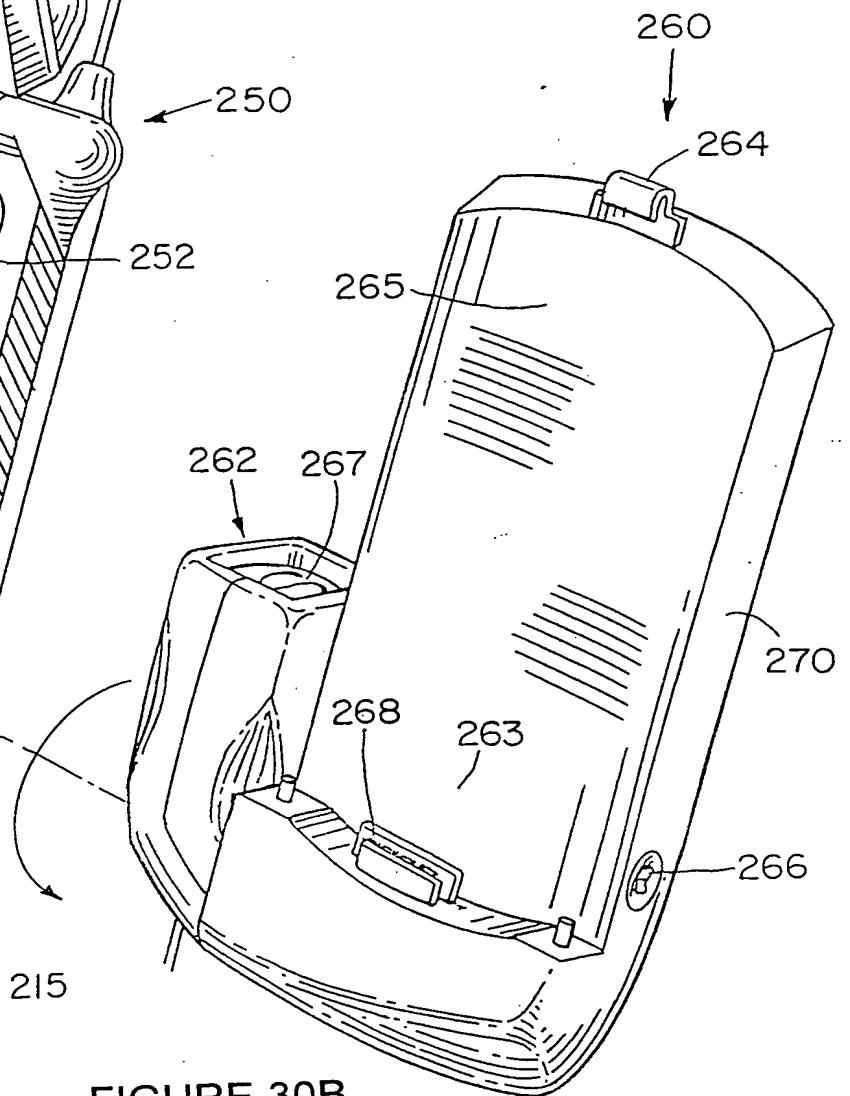


FIGURE 30B

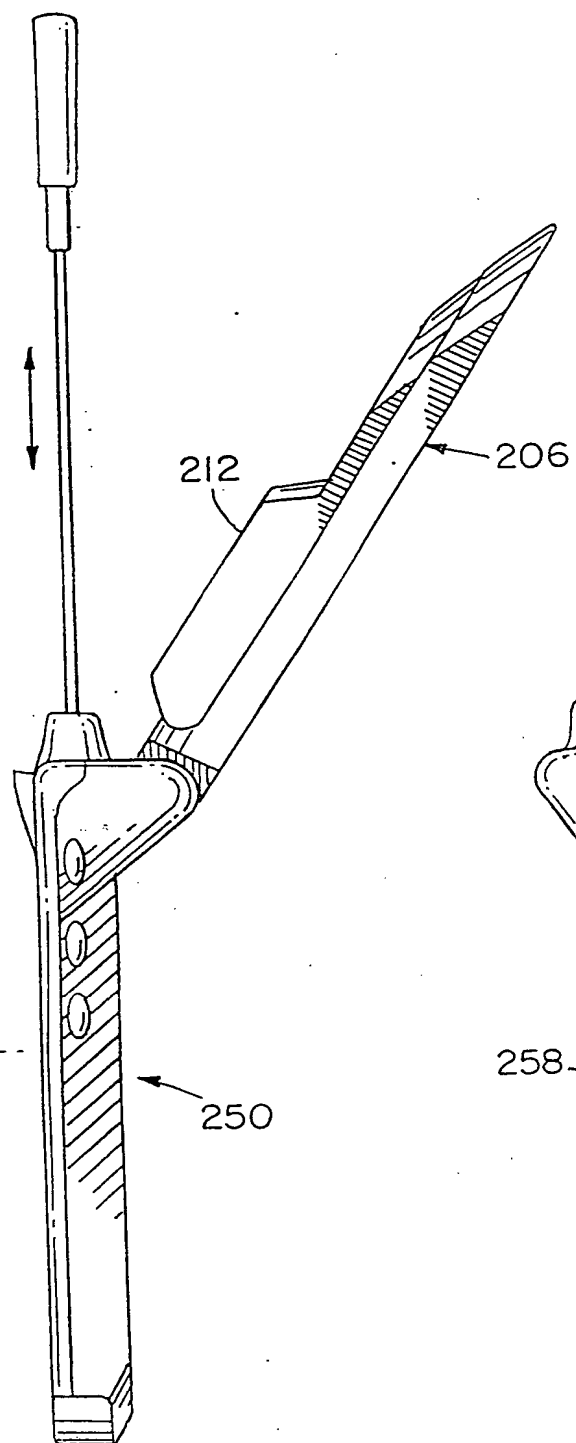


FIGURE 30C

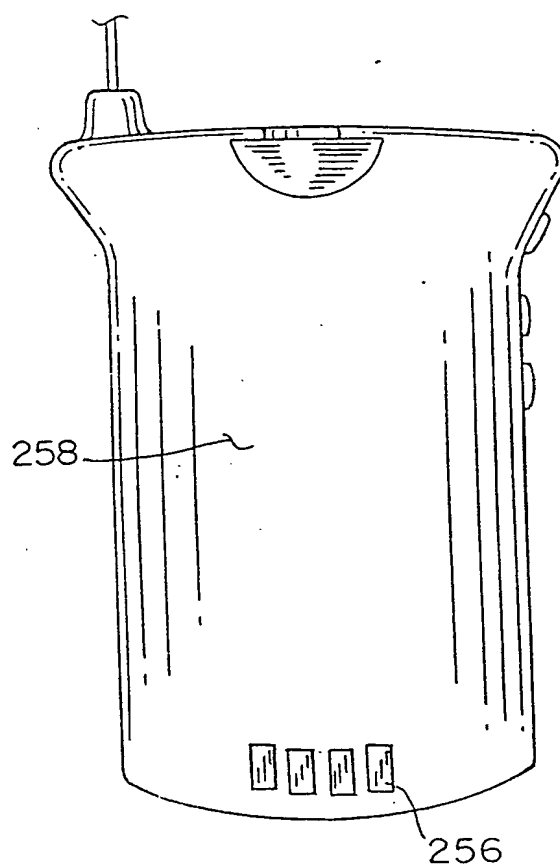


FIGURE 30D



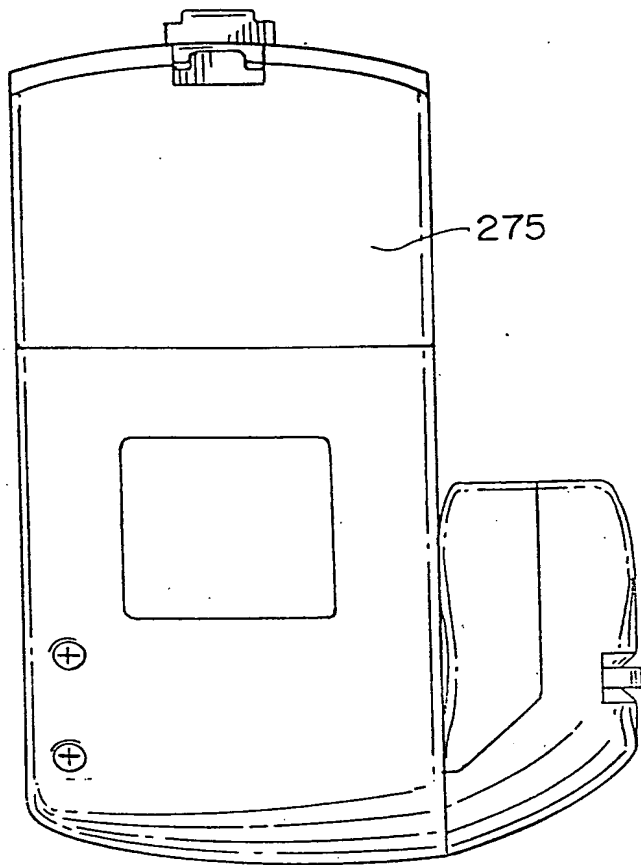


FIGURE 30H

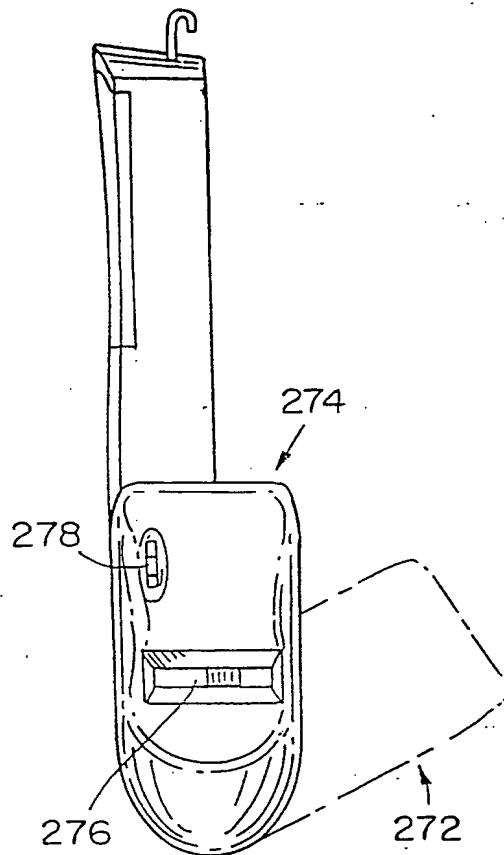


FIGURE 30I

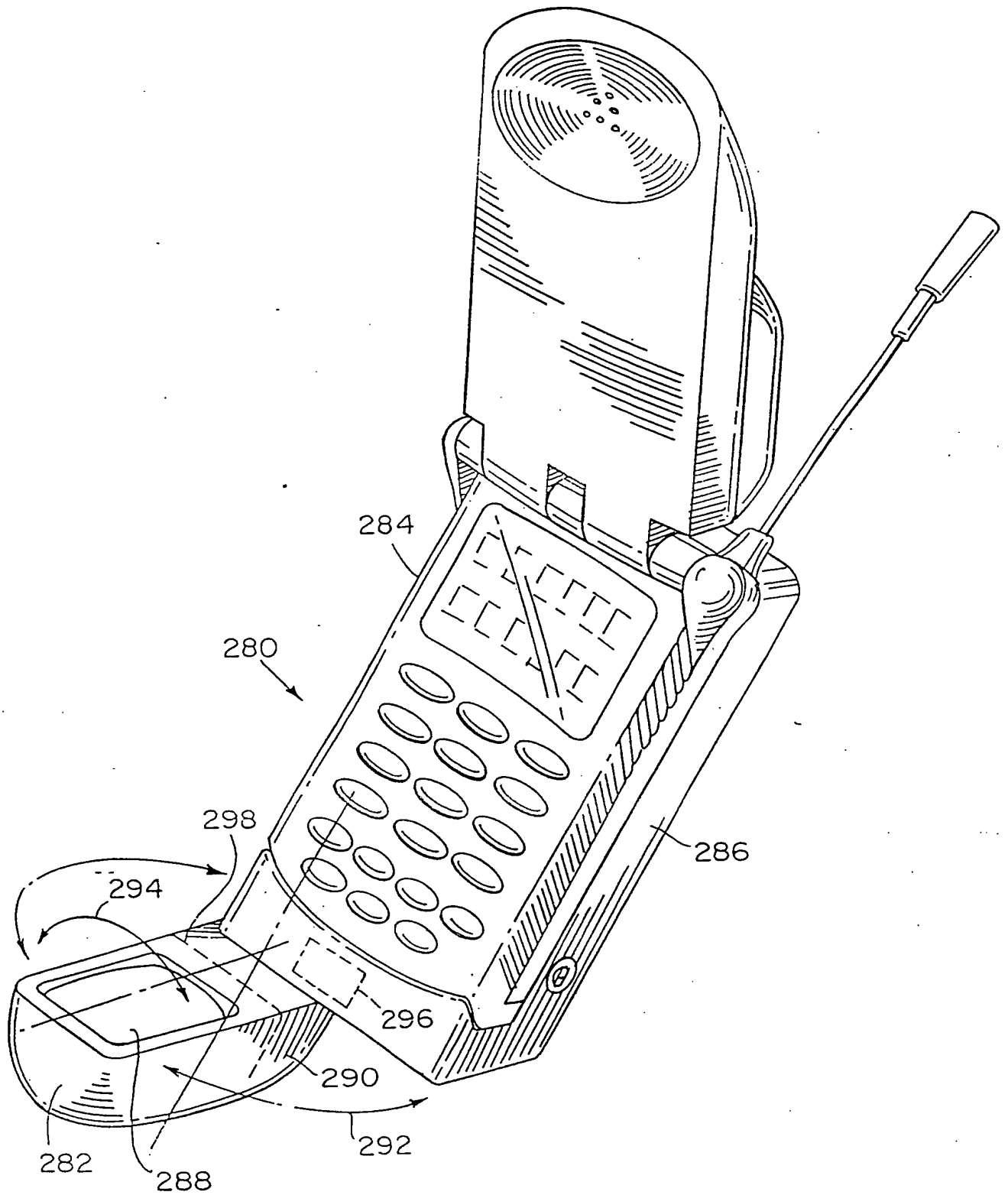


FIGURE 30J

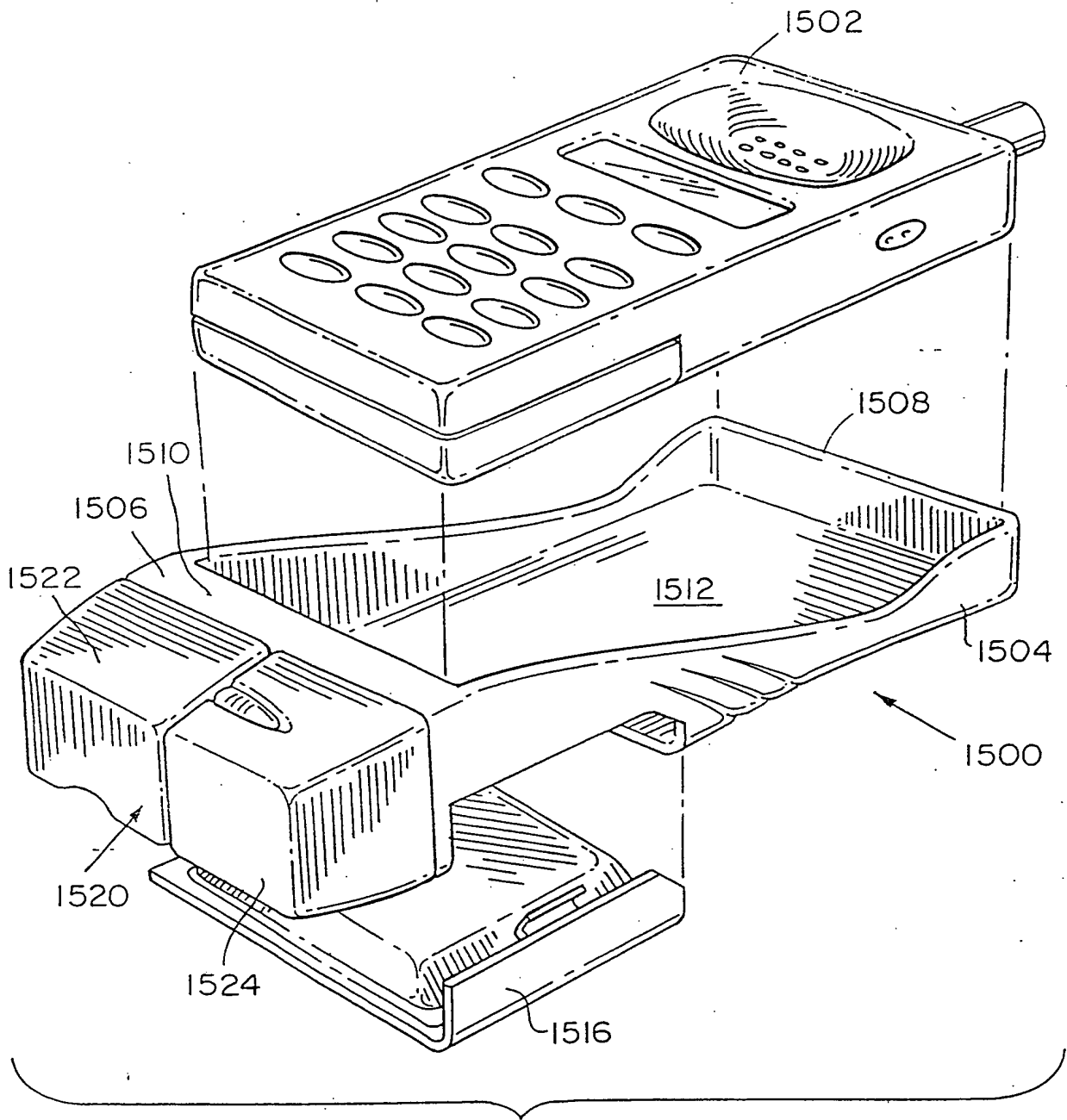


FIGURE 31A

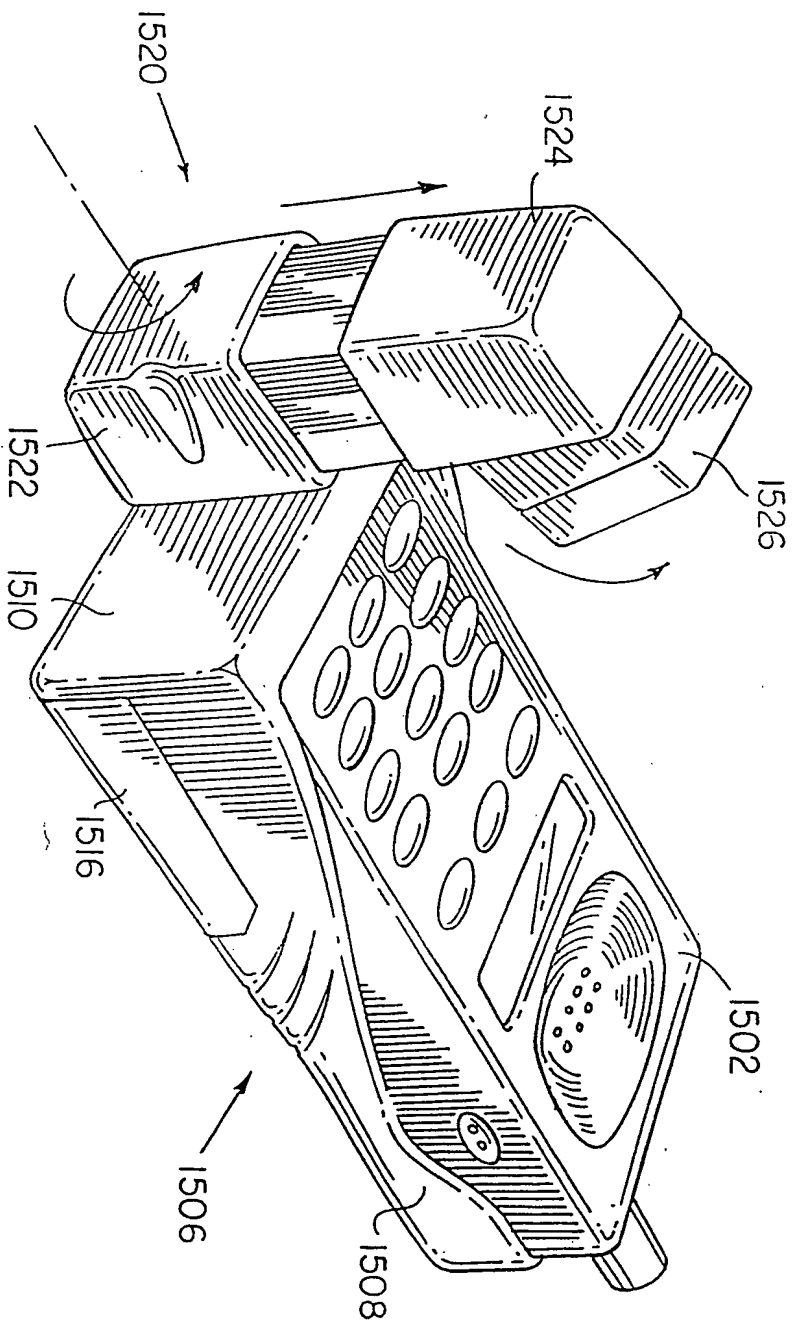


FIGURE 31B

864240" 19099060

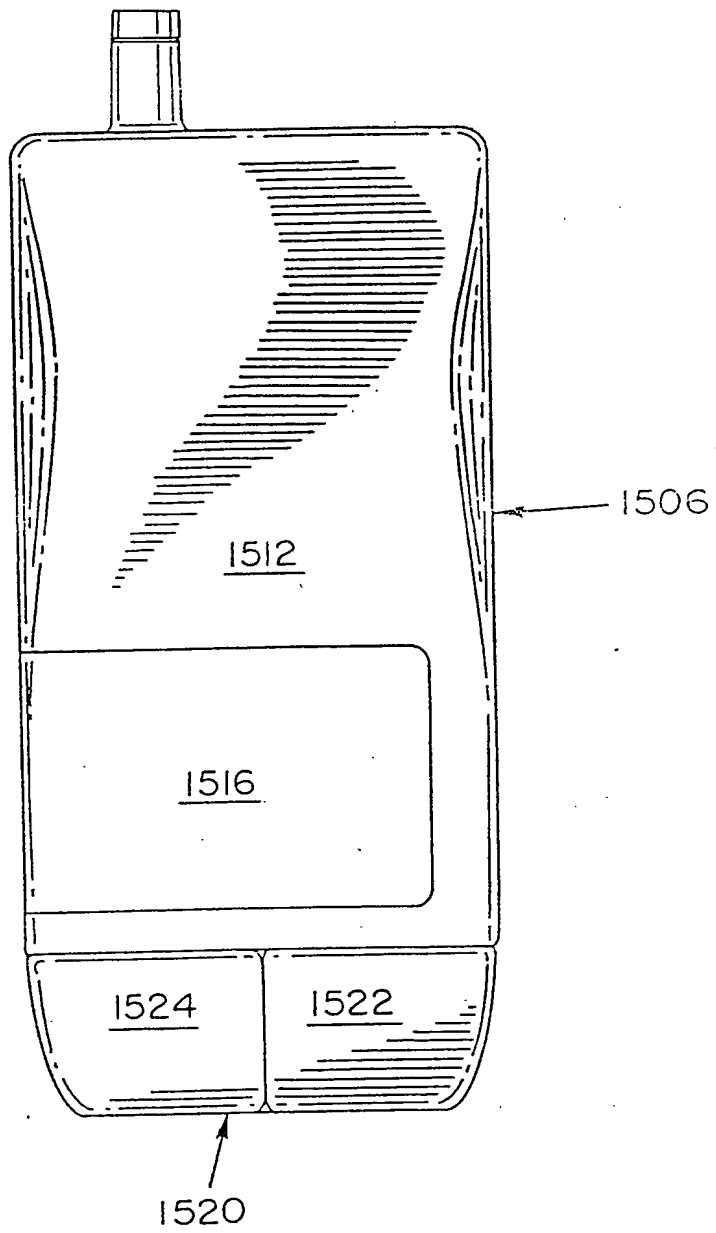


FIGURE 31C

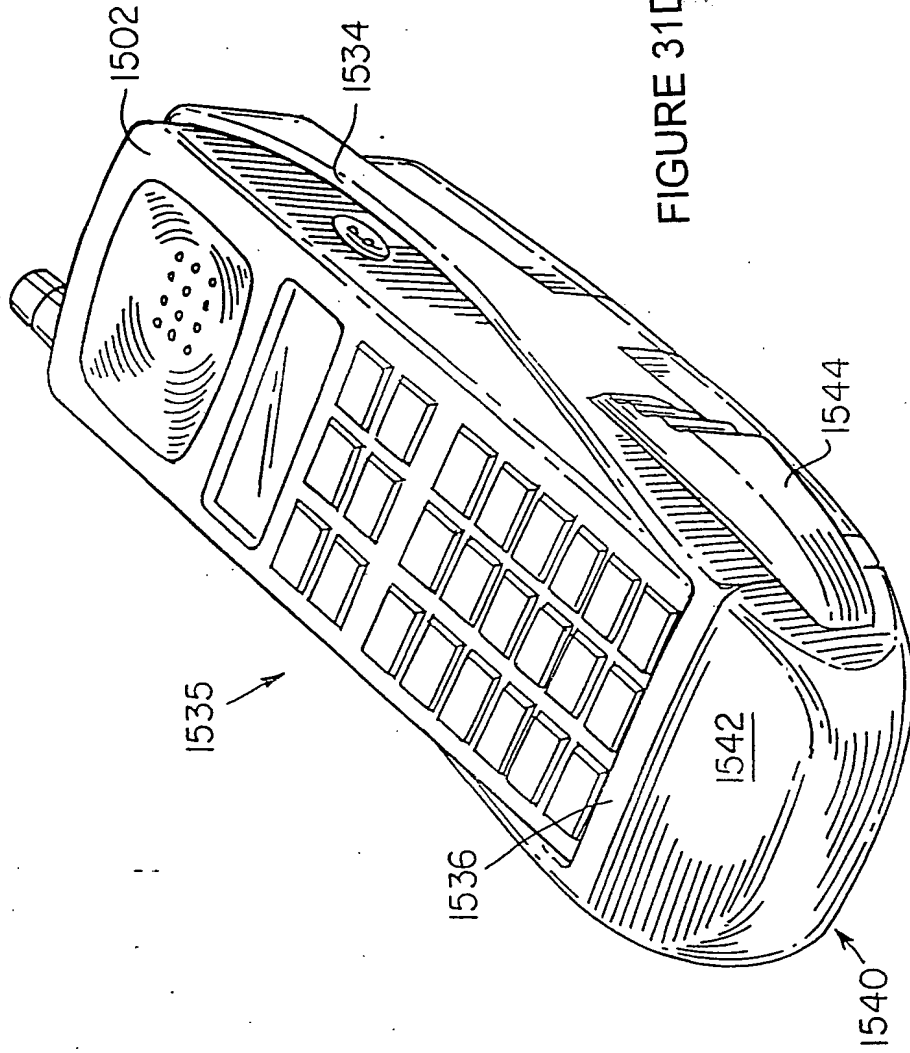


FIGURE 31D

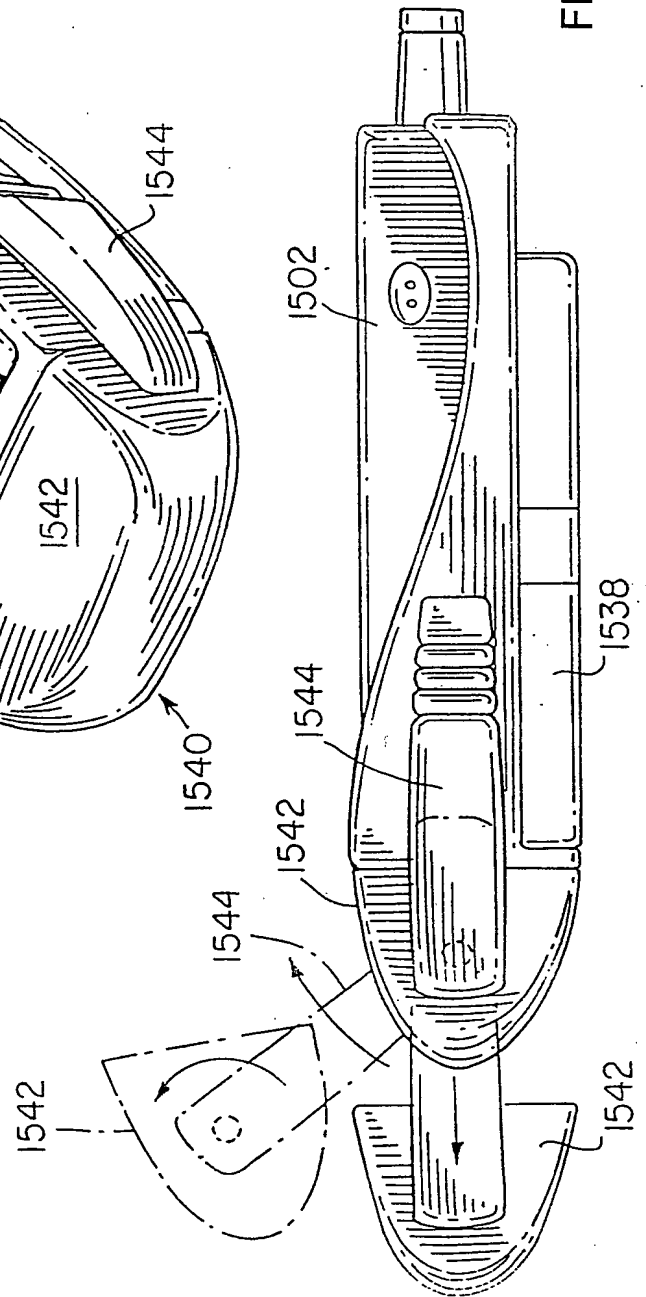


FIGURE 31E

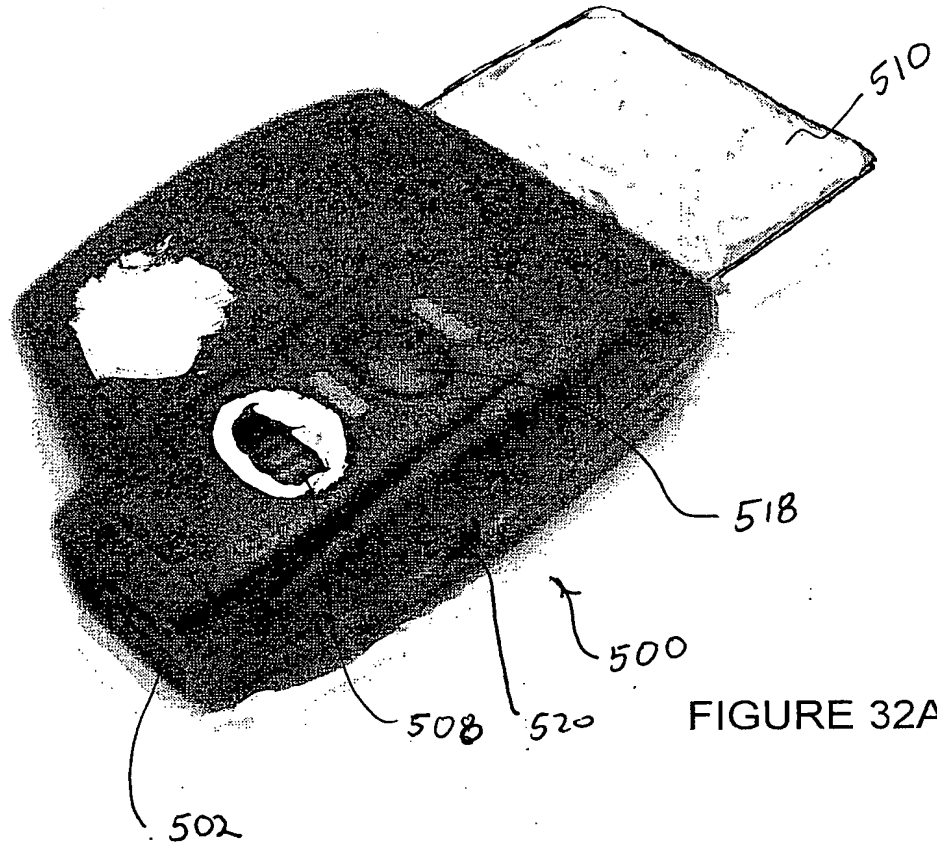


FIGURE 32A

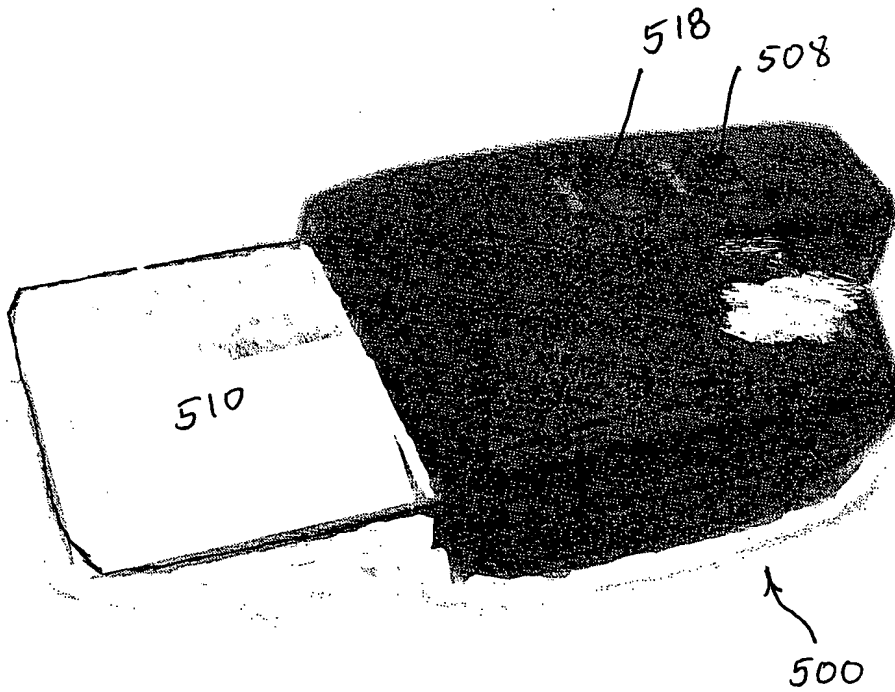


FIGURE 32B

09066061-042498

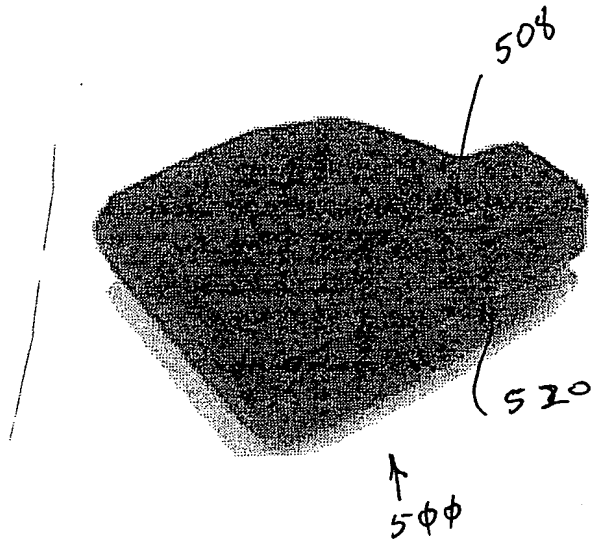


FIGURE 32C

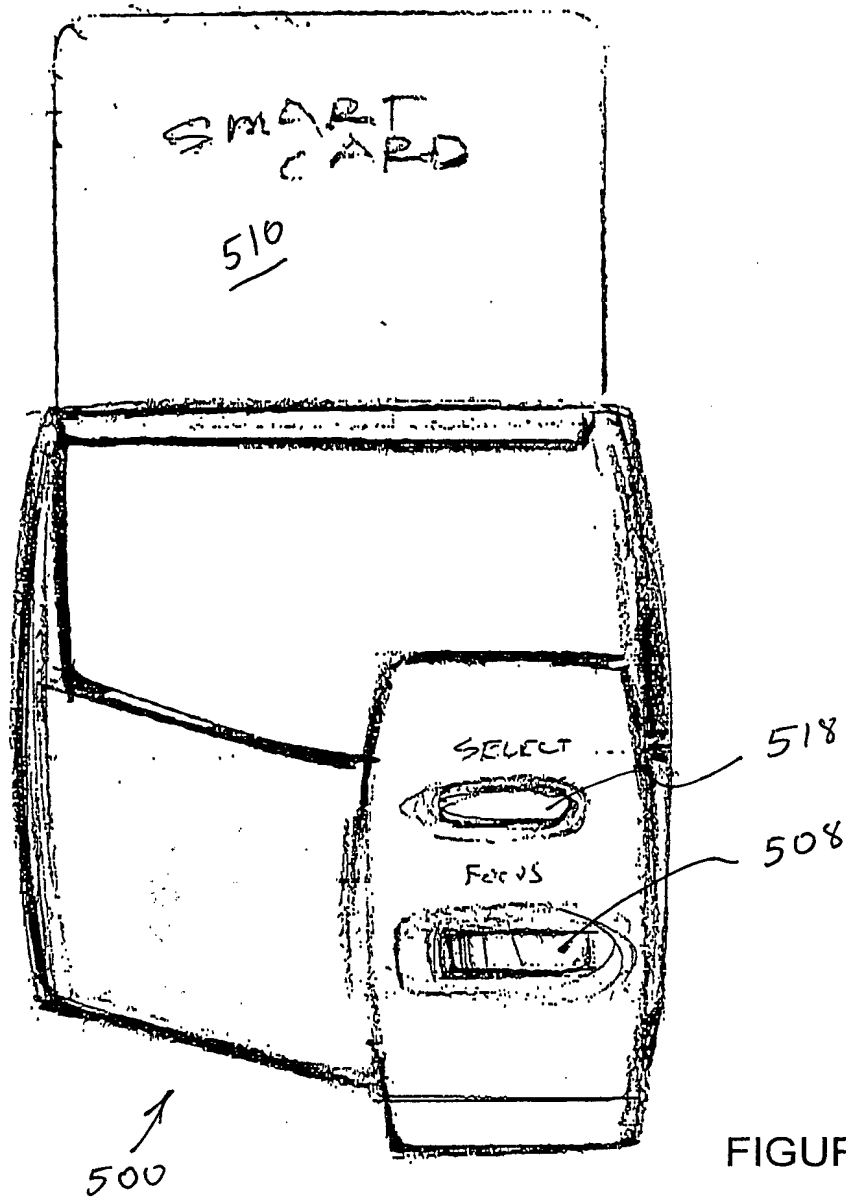


FIGURE 32D

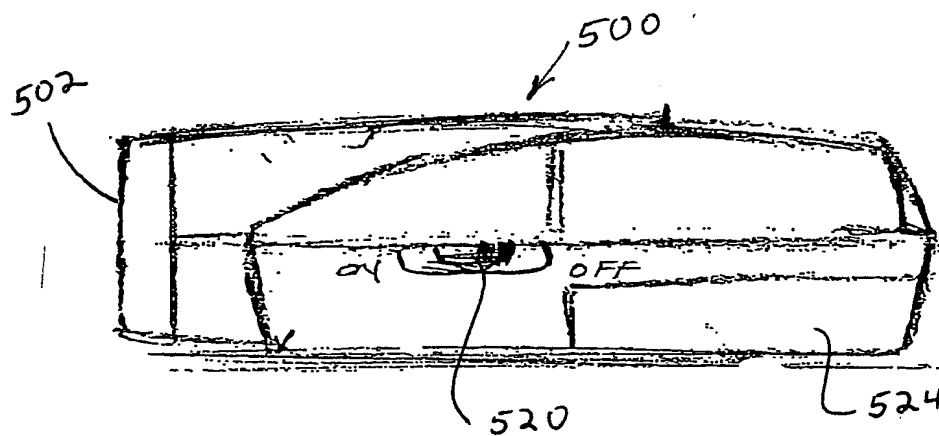
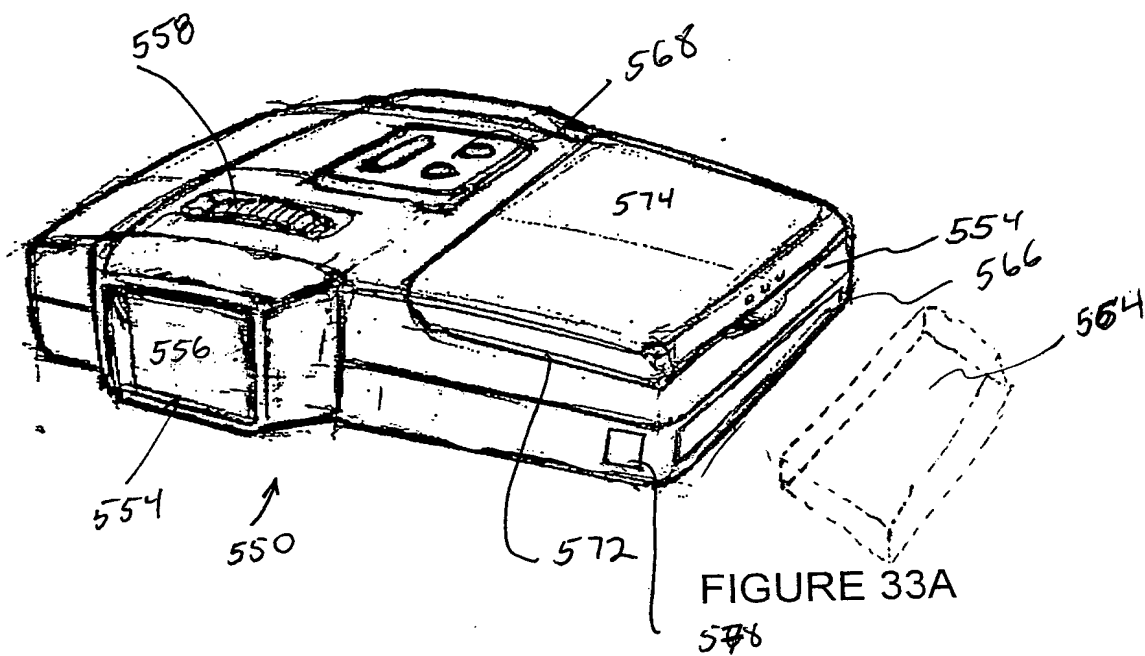


FIGURE 32E



0906061-042498

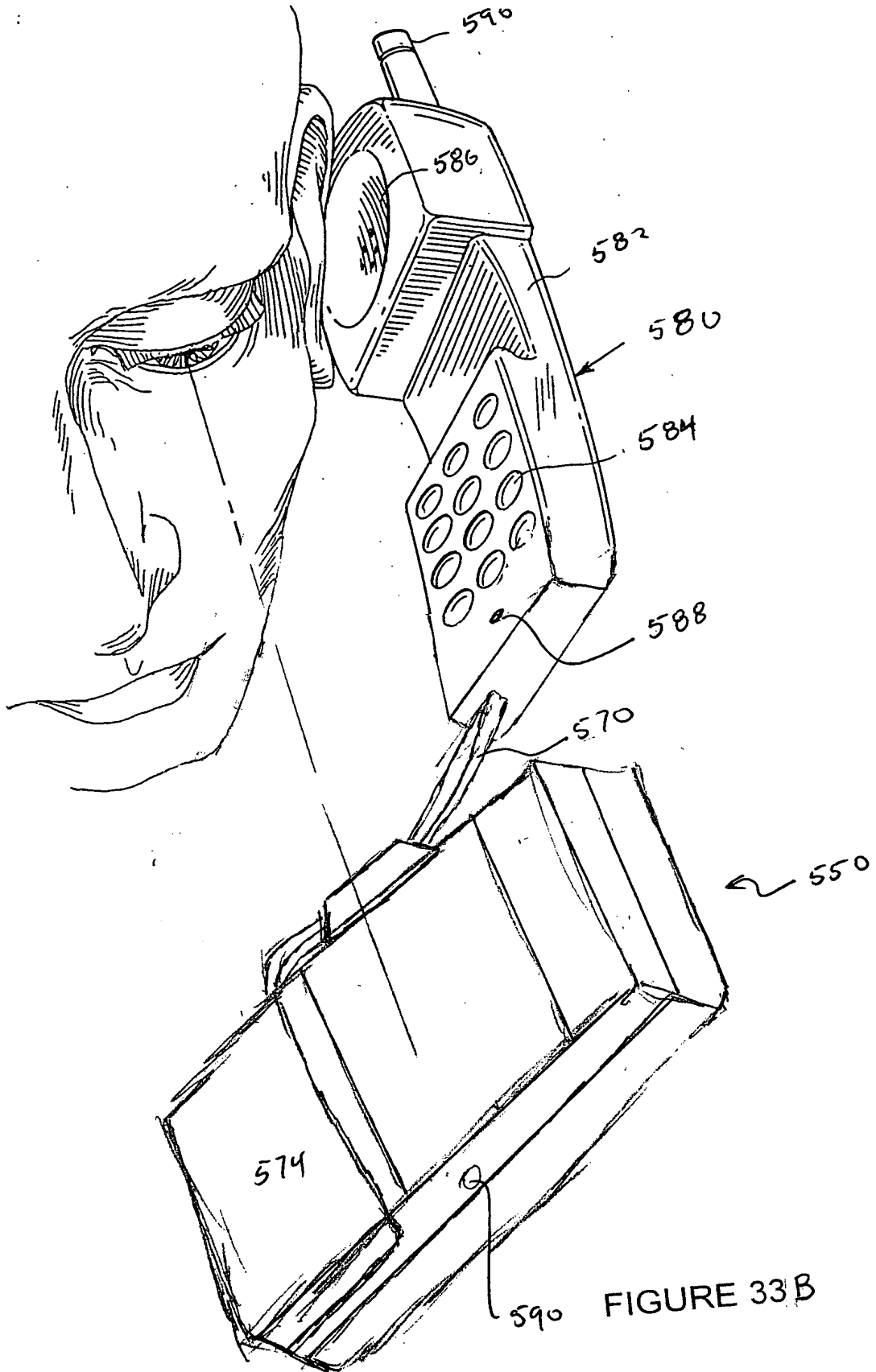


FIGURE 33B

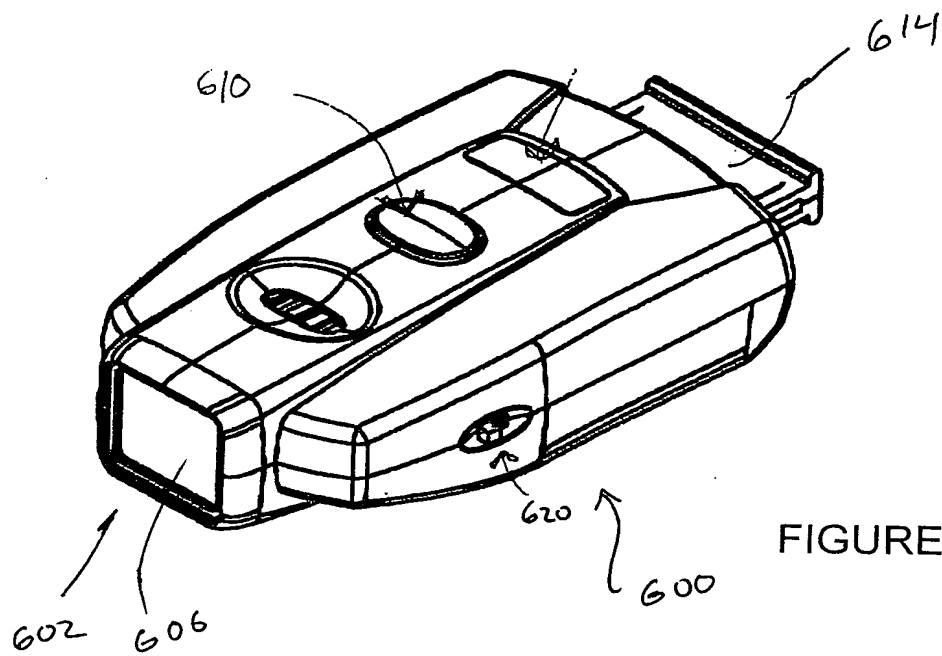


FIGURE 34A

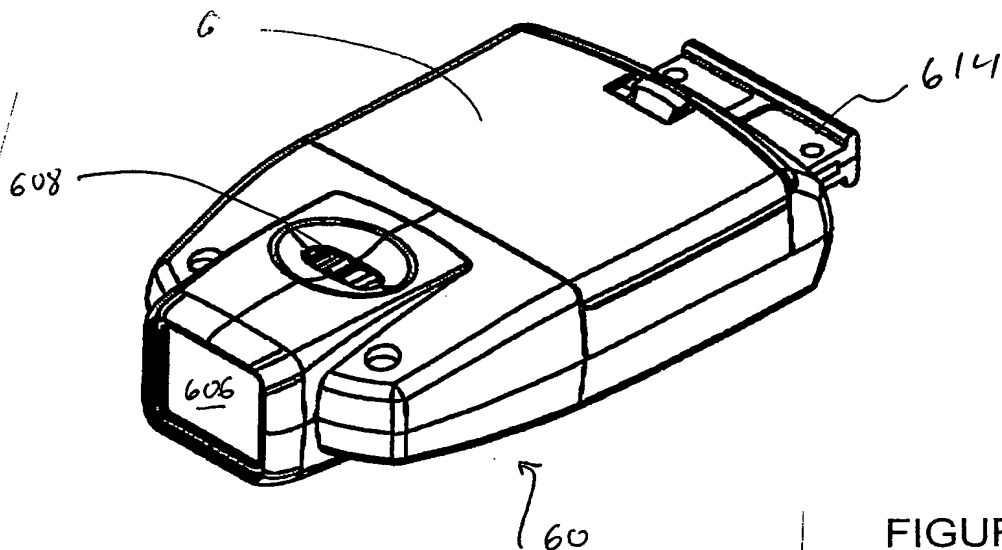


FIGURE 34B

04496061.01240" T.9093060

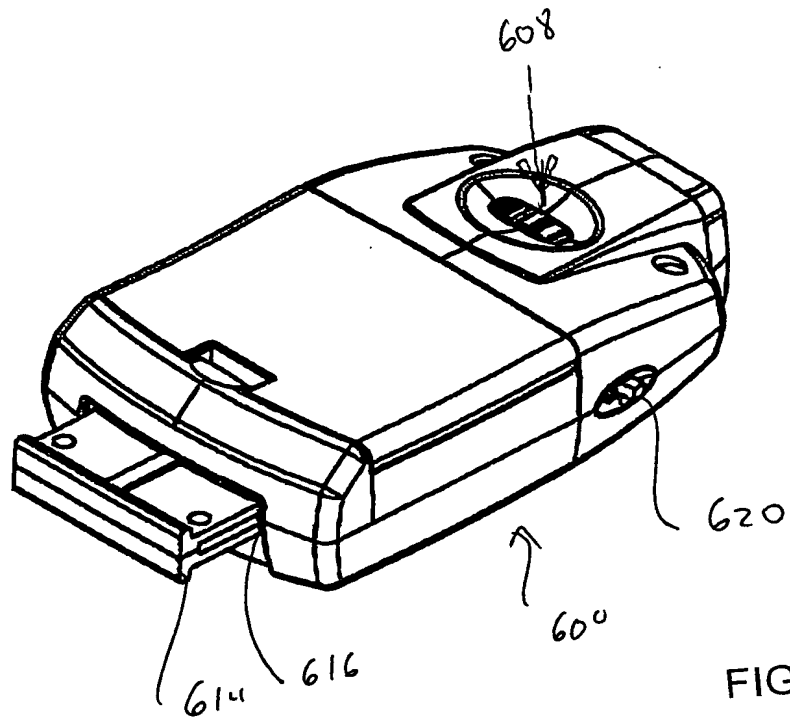


FIGURE 34C

09066061 042498 867270" T9099060

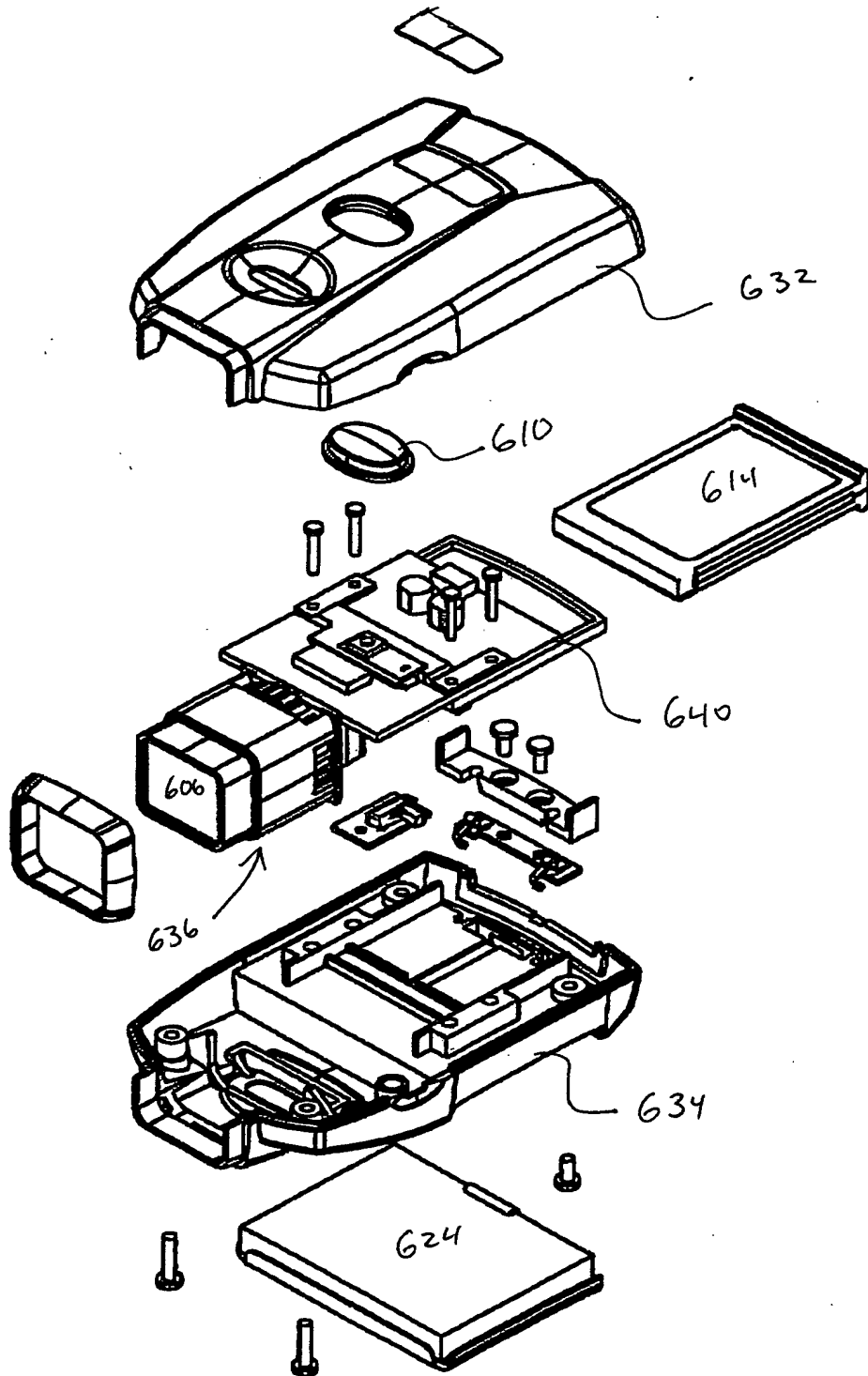


FIGURE 34D